

液晶模组规格书

LCM Specification

☐初定规格 Preliminary specification

☒正式规格 Official specifications

项目编号 Project No.	TFT-H101A9WSIFTKC40
产品描述 Product Description	TFT LCD Module 1024 x 3RGB x 600 Dots 10.1 Inch TFT LCD
客户确认签章: Signature by customer: ☐小批量试产 Trial production ☐大批量生产 Mass production	

鑫洪泰科技（广东）有限公司

深圳市鑫洪泰电子科技有限公司

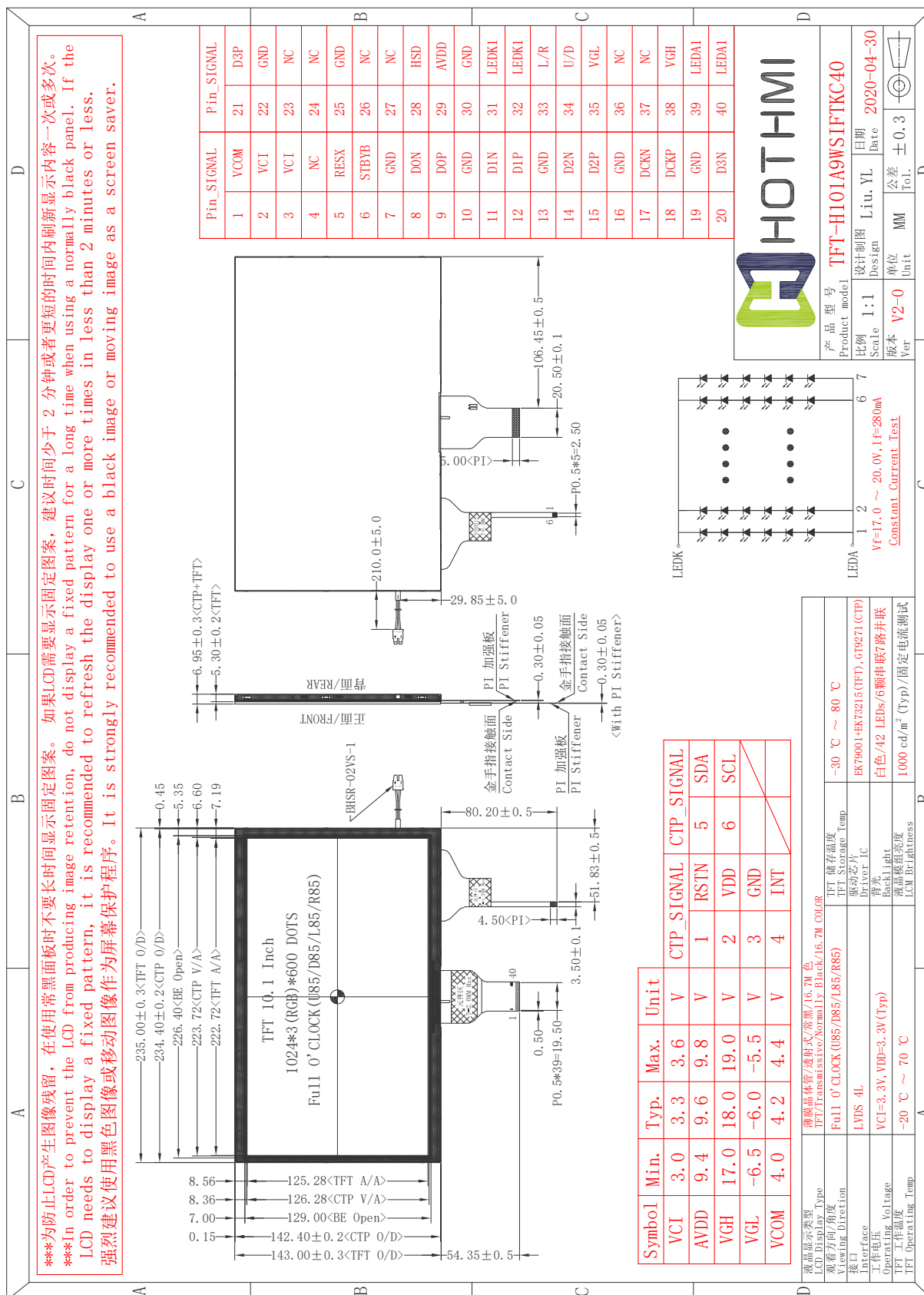
广东省深圳市龙岗区龙岗天安数码城 3 栋 B 座 1202-G

广东省东莞市凤岗镇东深公路 208 号天安数码城 N3 栋 3 楼

一、基本特征 General Feature:

项目 Item	标准值 Standard Value	单位 Unit
显示尺寸 Display Size	10.1	英寸 Inch
分辨率 Number of Pixels	1024 (H) * 3 (RGB) * 600 (V)	点 dots
显示区域 Active Area	222.72 (H) * 125.28 (V)	毫米 mm
外形尺寸 Outline Dimension	235.00 (H) * 143.00 (V) * 5.30 (D) <TFT> 235.00 (H) * 143.00 (V) * 6.90 (D) <TFT+RTP> 235.00 (H) * 143.00 (V) * 6.95 (D) <TFT+CTP>	毫米 mm
观看方向 Viewing Direction	全 视角 FULL 0' clock	—
TFT 接口 TFT Interface	LVDS (VESA 标准 VESA standard)	—
TFT 驱动芯片 TFT Driver IC	EK79001+EK73215	—
TFT 驱动电压 TFT Driver Condition	VCI=3.3V (Typ)	伏 V
背光 Backlight	白色 LED/42 颗/6 LEDs 串联 7 路并联 White LED/42 PCS/6 LEDs serial 7 ways parallel	—
触摸屏 Touch Panel	☒ 不带触摸屏 ☒ 带电阻触摸屏 ☒ 带电容触摸屏 Without TP With RTP With CTP	—
电容触摸屏驱动芯片 CTP Driver IC	GT9271	—
电容触摸屏驱动电压 CTP Driver Condition	VDD=3.3V (Typ)	伏 V
TFT 液晶工作温度 Operation Temperature	-20 ~ 70	摄氏度 ℃
TFT 液晶储存温度 Storage Temperature	-30 ~ 80	摄氏度 ℃

III: TFT-H101A9WSIFTKC40_V2-0(With CTP)



三、引脚说明 Pin Description

3.1. 模组引脚说明 TFT Pin Description

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
1	VCOM	TFT Common Electrode Voltage
2~3	VCI	Digital Power
4	NC	No Connect
5	RESX	Global reset pin. Active low to enter reset state. Usually pulled high. For stability, an RC delay circuit needs to be connected externally. (RESX delay VCI greater than 1ms)
6	STBYB	Standby mode, normally pull high. STBYB = "VCI", normal operation STBYB = "GND", Standby mode
7	GND	Ground
8	D0N	LVDS negative differential data
9	D0P	LVDS positive differential data
10	GND	Ground
11	D1N	LVDS negative differential data
12	D1P	LVDS positive differential data
13	GND	Ground
14	D2N	LVDS negative differential data
15	D2P	LVDS positive differential data
16	GND	Ground
17	DCKN	LVDS negative differential clock
18	DCKP	LVDS positive differential clock
19	GND	Ground
20	D3N	LVDS negative differential data
21	D3P	LVDS positive differential data
22	GND	Ground
23~24	NC	No Connect
25	GND	Ground
26~27	NC	No Connect
28	HSD	Data channel selection STBYB = "VCI", 6 bit STBYB = "GND", 8 bit
29	AVDD	Analog Power
30	GND	Ground
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引脚编号 Pin NO.	标号 Symbol	详细描述 Description
31~32	LEDK	LED Cathode, Not connected here
33	L/R	Horizontal scan direction control pin L/R= "VCI", From left to right(Default) L/R= "GND", From right to left
34	U/D	Vertical scan direction control pin U/D= "VCI", From down to up. U/D= "GND", From up to down. (Default)
35	VGL	TFT Gate OFF Voltage
36~37	NC	No Connect
38	VGH	TFT Gate ON Voltage
39~40	LEDA	LED Anode, Not connected here
- END -		

3.2. CTP 引脚说明 CTP Pin Description

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
1	RSTN	An external low signal reset the chip.
2	VDD	Digital Power
3	GND	Ground
4	INT	Interrupt signal
5	SDA	IIC data signal
6	SCL	IIC clock signal
- - - END - - -		

3.3. RTP 引脚说明 RTP Pin Description

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
1	XL	X-axis measuring foot
2	YD	Y-axis measuring foot
3	XR	X-axis measuring foot
4	YU	Y-axis measuring foot
- - - END - - -		

3.4. 背光引脚说明 Backlight pin description

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
1	LEDA	LED Anode, red line or white line
2	LEDK	LED Cathode, black line
- - - END - - -		

连接器型号 Connector Model Number: BHSR-02VS-1

配合型号 Mating Model Number: BHSMR-02VS

3.5. 接线说明 Wiring instructions

