

PRODUCT SPECIFICATION

TFT LCD MODULE

MODEL : KWH013ST03-F01 Version: 1.0

【 ◆ 】 Preliminary Specification

【 】 Finally Specification

CUSTOMER'S APPROVAL	
SIGNATURE:	DATE:

- It signifies that you fully understand and accept all the contents of this specification if you sign and send back the first page of this specifications.

Designed by	R&D Checked by	Quality Department by	Approved by
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1. GENERAL INFORMATION 主要特征描述

Item 项目	Contents 内容	Unit 单位
LCD Type 液晶显示类型	TFT/TRANSMISSIVE	---
Viewing Direction 视角方向	ALL	O'Clock
Outline Dimensions (W × H×T) 外形尺寸 (宽 x 高 x 厚)	26.85(W) × 29.55 (H) × 1.98(T)	mm
Viewing area 可视区域	-	mm
Active area 有效区域 (宽 × 高)	23.40(W) x 23.40(H)	mm
Number of Dots 点阵	240RGB x 240Dots	---
Pixel pitch (W × H) 像素点尺寸	0.0975H) x 0.0975(V)	mm
Driver IC 驱动 IC	JD9851	---
Interface Type 接口类型	SPI4	---
Input voltage 输入电压	2.8V	---
Module Power consumption 模块功耗	TBD	MW
Colors 色彩	262K	---
Backlight Type 背光类型	LED	---

3. ABSOLUTE MAXIMUM RATINGS 极限参数

Item 项目	Symbol 符号	Min 最小值	Max 最大值	Unit 单位
Supply voltage for logic 逻辑电压	VDD	2.5	3.3	V
Input voltage 输入电平	VIN	-0.3	VDD+ 0.3	V
Operating temperature 使用温度	TOP	-20	70	°C
Storage temperature 存储温度	TST	-30	80	°C
Humidity 湿度	RH		90%(Max60°C)	RH

4. ELECTRICAL CHARACTERISTICS 模块电气特性

Item 项目	Symbol 符号	Min 最小值	Typ 典型值	Max 最大值	Unit 单位
Supply voltage for logic 逻辑电压	VDD	2.5	2.8	3.3	V
Input Current 输入电流	Idd		TBD	TBD	mA
Input voltage 'H' level 输入高电平	VIH	0.7VDD	-	VDD	V
Input voltage 'L' level 输入低电平	VIL	VSS		0.3VDD	V
Output voltage 'H' level 输出高电平	VOH	0.8VDD		VDD	V
Output voltage 'L' level 输出低电平	VOL	VSS		0.2VDD	V

5. BACKLIGHT CHARACTERISTICS 背光电气特性

Item 项目	Symbol 符号	Min 最小值	Typ 典型值	Max 最大值	Unit 单位	Condition 条件
Forward voltage 正向电压	Vf	3.0	3.2	3.4	V	If=2*20mA
Number of LED 数量	LED		2		Piece	
Connection mode 连接类型	P		Parallel			

Using condition: constant current driving method If=40mA(+/-10%).

使用条件: 恒流的驱动方式是 If=40mA(+/-10%).

6.ELECTRO-OPTICAL CHARACTERISTICS

光电参数

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
Transmittance(LCM)	T(%)	2.5	3.5		%	
Contrast Ratio (LCM)	C/R	400	600			
Response Time (TFT)	Tr+Tf		30	35	msec	
Surface Luminance (LCM)	lv	200	250		cd/m ²	
CIE Color Coordinate (LCM)	Rx	0.556	0.606	0.656	-	
	Ry	0.277	0.327	0.377	-	
	Gx	0.258	0.308	0.358	-	
	Gy	0.513	0.563	0.613	-	
	Bx	0.096	0.146	0.196	-	
	By	0.110	0.160	0.210	-	
	Wx	0.22	0.27	0.32	-	
	Wy	0.23	0.28	0.33	-	
Viewing Angle (TFT)	θ l	70	80		Degree	
	θ r	70	80			
	θ u	70	80			
	θ d	70	80			

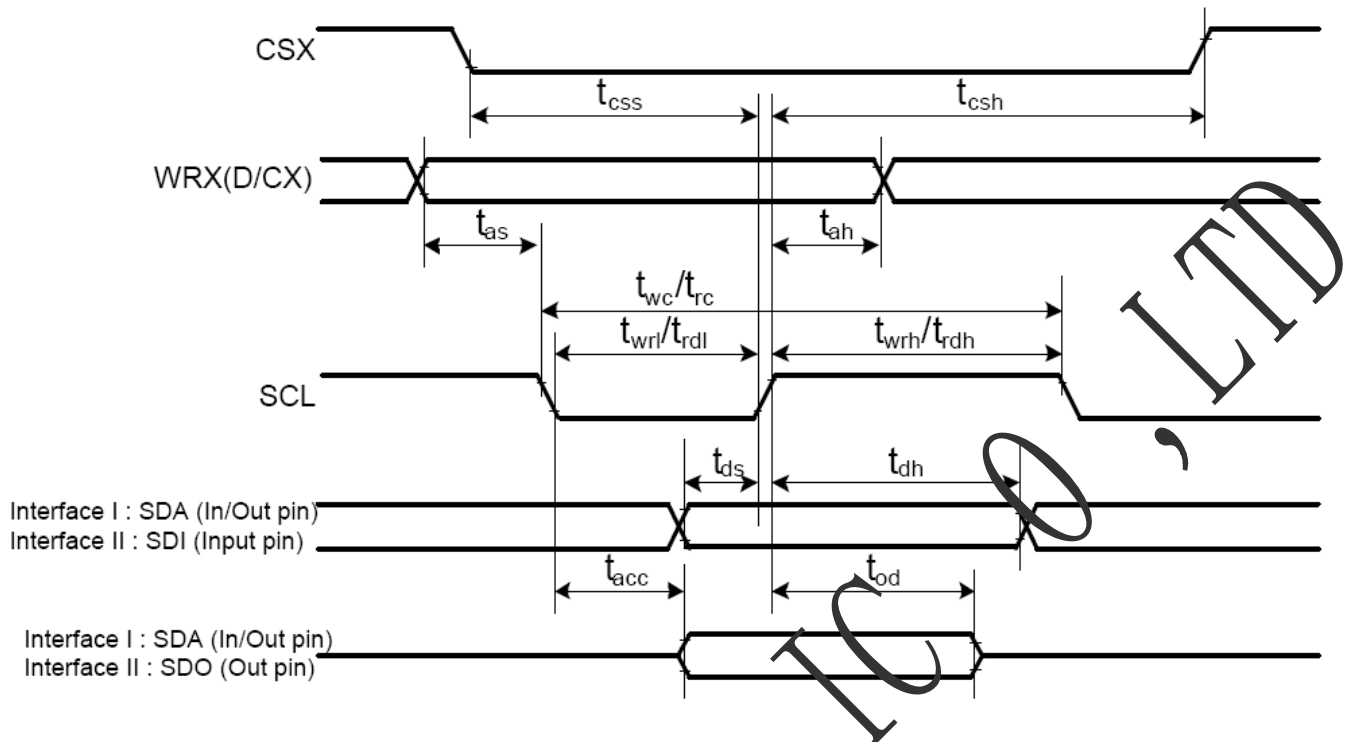
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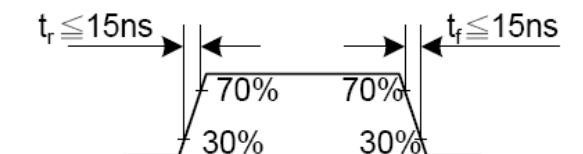
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7. READ/WRITE TIMING 读/写时序



Signal	Symbol	Parameter	min	max	Unit	Description
CSX	t_{css}	Chip select time (Write)	15	-	ns	
	t_{csh}	Chip select hold time (write)	15	-	ns	
SCL	t_{wc}	Serial clock cycle (Write)	66	-	ns	
	t_{wrh}	SCL "H" pulse width (Write)	33	-	ns	
	t_{wrl}	SCL "L" pulse width (Write)	33	-	ns	
	t_{wc}	Serial clock cycle (Write RGB data)	15	-	ns	MTK-2 lane mode only
	t_{wrh}	SCL "H" pulse width (Write RGB data)	4	-	ns	MTK-2 lane mode only
	t_{wrl}	SCL "L" pulse width (Write RGB data)	4	-	ns	MTK-2 lane mode only
	t_{rc}	Serial clock cycle (Read)	150	-	ns	
	t_{rdh}	SCL "H" pulse width (Read)	75	-	ns	
	t_{rdl}	SCL "L" pulse width (Read)	75	-	ns	
	t_{as}	D/CX setup time	10	-	ns	
D/CX	t_{ah}	D/CX hold time (Write / Read)	10	-	ns	
	t_{ds}	Data setup time (Write)	30	-	ns	
SDA / SDI (Input)	t_{dh}	Data hold time (Write)	30	-	ns	
	t_{acc}	Access time (Read)	10	-	ns	For maximum CL=30pF
SDA / SDO (Output)	t_{od}	Output disable time (Read)	10	70	ns	For minimum CL=8pF

Note: $T_a = 25^\circ\text{C}$, $IOVCC=1.65\text{V to }3.3\text{V}$, $VCI=2.5\text{V to }3.3\text{V}$, $AGND=GND=0\text{V}$



NOTE :This section is only for reference,Details please refer to the IC specification.

备注：本节仅供参考，详细信息请参阅 IC 规格书

8. INTERFACE DESCRIPTION 接口定义描述

Pin No.	Symbol	I/O	Function
1	GND		Ground
2	LED K		BL LED power-
3	LED A		BL LED power+
4	VDD2.8V		Power supply 2.8V
5	GND		Ground
6	GND		Ground
7	A0		Serial signal input pin
8	CS		Chip enable select pin L: enable
9	SCLK		Serial clock signal input pin
10	SDA		Serial signal input/output pin
11	RESET		System reset select pin
12	GND		Ground

Note: Logical Voltage (IOVCC) Depends on the High Level of Platform I/O Port
备注: 逻辑电压IOVCC需要和平台I/O口高电平保持一致

9. REFERENCE APPLICATION CIRCUIT

参考应用电路

Please consult our technical department for detail information.
 详细信息请联系我们的技术部门

10. RELIABILITY TEST CONDITIONS 可靠性试验条件

No. 序号	Test Item 试验项目	Test condition 试验条件	Inspection after test 判断标准
1	High Temperature Storage 高温存放	80℃±2℃ 24H	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 试验结束后, 已测试的 LCD 样品必须在室内正常温湿度环境下放置 2~4 个小时以上才能进行功能和外观检查, 样品不允许有以下缺陷: 1.Air bubble in the LCD; 模块中有气泡; 2.Sealleak; 封口松脱; 3.Non-display; 不显示; 4.missing segments; 漏笔 5.Glass crack; 玻璃破碎; 6.Current Idd is twice higher than initial value. 电流 Idd 大于初时值的 2 倍
2	Low Temperature Storage 低温存放	-30℃±2℃ 24H	
3	High Temperature Operation 高温操作	70℃±2℃ 24H	
4	Low Temperature Operation 低温操作	-20℃±2℃ 24H	
5	High Temperature /Humidity Storage 高温高湿	40℃±2℃ 90%RH 24H	
6	Temperature Cycle 冷热循环	-30℃→25℃→80℃ 5min 30min ←25℃, 5min after 10cycle, Restore 4H at 25℃	Not allowed cosmetic and electrical defects.
7	Vibration Test (package state) 振荡试验	10Hz~150Hz, 100m/s ² , 120min	
8	Dropping test 跌落试验	Drop to the ground from 0.5m height, one time, every side of carton. (Packing condition)	
9	ESD test 静电试验	Voltage:±4KV R: 330Ω C: 150pF Air discharge, 10time	

11. INSPECTION CRITERION 检查标准

Please consult our Quality Department for detail information.
 详细信息请联系我们的品质部门

12. PRECAUTIONS FOR USE OF LCD MODULES

12.1 Handling Precautions

12.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

12.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

12.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

12.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

12.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

— Isopropyl alcohol

— Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

— Water

— Ketone

— Aromatic solvents

12.1.6 Do not attempt to disassemble the LCD Module.

12.1.7 If the logic circuit power is off, do not apply the input signals.

12.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

a. Be sure to ground the body when handling the LCD Modules.

b. Tools required for assembly, such as soldering irons, must be properly ground.

c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

12.2 Storage precautions

12.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

12.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ~ 40℃

Relatively humidity: ≤80%

12.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

12.3.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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