

液晶模组说明书

LCD Module Instructions

☒初定规格 Preliminary specification

☐正式规格 Official specifications

项目编号 Project No.	TFT-H070A20 系列
产品描述 Product Description	TFT LCD Module 1024 (RGB) x 600 Pixels 7.0 Inch TFT LCD
客 户 确 认 签 章: Signature by customer:	
☐小批量试产 Trial Production ☐大批量生产 Mass Production 请返回一份带有您的签名和评论的确认副本 Please return one copy confirmation with signature and your comments	

HotDisplay Technology Co., Ltd

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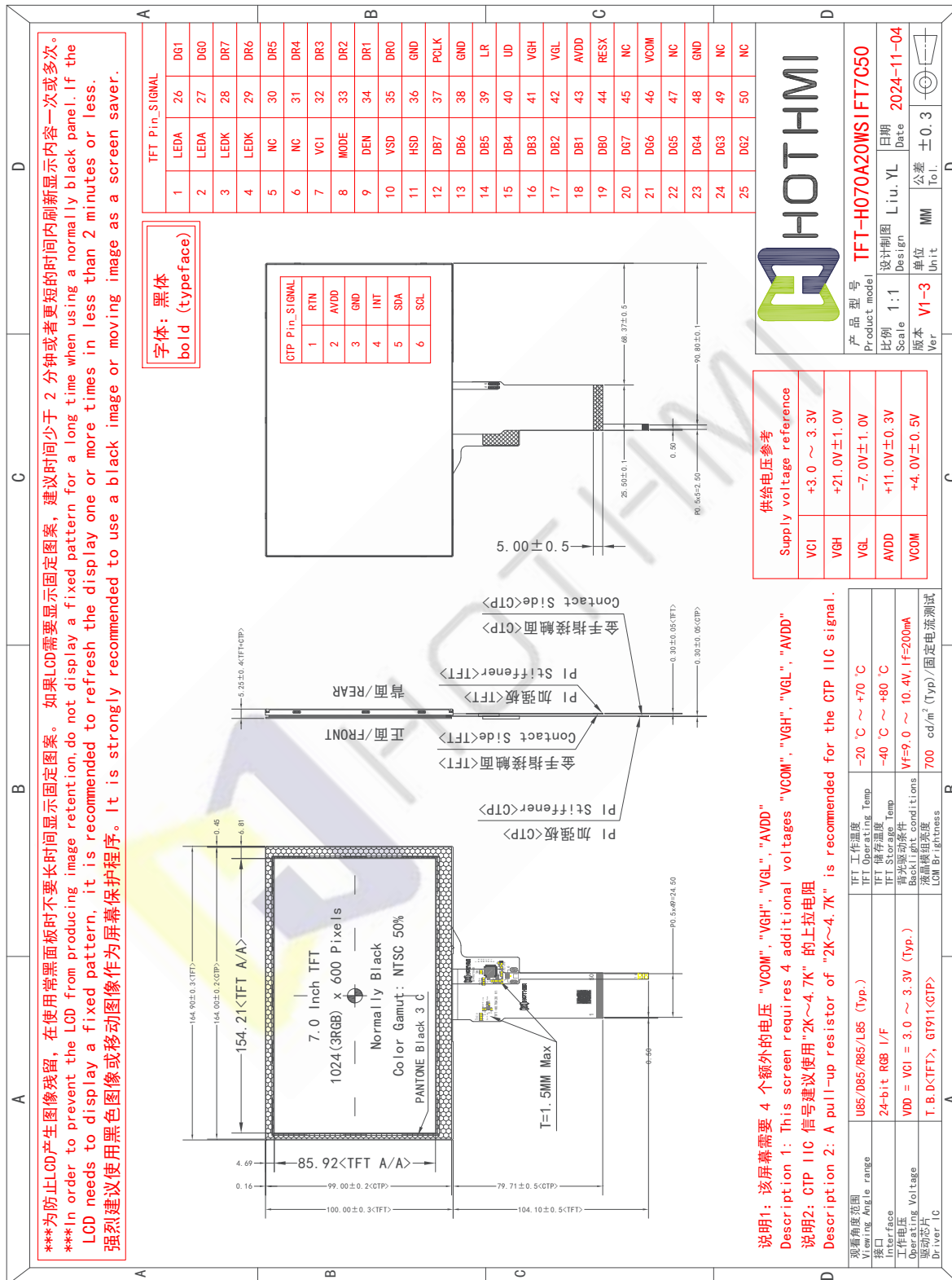
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一、基本特征 General Feature:

项目 Item	标准值 Standard Value			单位 Unit
TFT 显示尺寸 TFT Display Size	7.0			英寸 Inch
TFT 分辨率 TFT Number of Pixels	1024 (RGB) (H) x 600 (V)			像素 Pixel
TFT 显示有效区域 TFT Display Active Area	154.21 (H) x 85.92 (V)			毫米 mm
TFT 模组外形尺寸 TFT Module Dimensions	164.90 (H) x 100.00 (V) x 3.45 (D) <TFT> 186.70 (H) x 1111.15 (V) x 5.50 (D) <TFT+CTP-1> 164.90 (H) x 100.00 (V) x 5.25 (D) <TFT+CTP-2>			毫米 mm
TFT 观看方向 TFT Viewing Direction	U85/D85/R85/L85 (Typ.)			度数 Deg.
TFT 模组接口 TFT Module Interfaces	24-bit RGB I/F			—
TFT 驱动芯片 TFT Driver IC	—			—
电容触摸屏驱动芯片 CTP Driver IC	GT911			
触摸屏 Touch Panel	☒ 不带触摸屏 Whitout TP	☐ 带电阻触摸屏 With RTP	☒ 带电容触摸屏 With CTP	—
TFT 模组重量 TFT Module Weight	不带触摸屏 Without TP	T. B. D ±5%		克 g
	带电容触摸屏 With CTP	T. B. D ±5%		
	带电阻触摸屏 With RTP	T. B. D ±5%		

说明 Description:



TFT-H070A20WSIFT7C50_V1-3<With CTP-2/700cd/m²>

三、引脚说明 Pin Description

3.1 模组引脚说明 TFT Pin Description

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
1~2	LEDA	LED阳极 LED anode
3~4	LEDK	LED阴极 LED cathode
5~6	NC	不连接 Not connected
7	VCI	数字电源电压 Digital supply voltage
8	MODE	DE / SYNC 模式选择 DE / SYNC mode select MODE=VCI: DE 模式; MODE=GND: Sync 模式。 MODE=VCI: DE mode; MODE=GND: Sync mode.
9	DEN	数据输入启用。高电平有效, 用于在 "DE 模式 "下启用数据输入总线。通常为低电平。 Data Input Enable. Active High to enable the data input bus under "DE Mode". Normally pull low.
10	VSD	垂直同步信号, 默认为负极性。 Vertical sync signal, default is negative polarity.
11	HSD	水平同步信号, 默认为负极性。 Horizontal sync signal, default is negative polarity.
12~19	DB[7~0]	用于 TTL 24 位并行 RGB 图像数据输入。 For TTL 24-bit parallel RGB image data input.
20~27	DG[7~0]	
28~35	DR[7~0]	
36	GND	电源地 Power supply ground
37	PCLK	输入数据的时钟。数据在该信号的上升沿/下降沿锁存。默认下降沿 Clock for Input Data. Data latched at rising/falling edge of this signal. Default falling edge
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引脚编号 Pin NO.	标号 Symbol	详细描述 Description
38	GND	电源地 Power supply ground
39	LR	信号源向右或向左序列控制 Source Right or Left sequence control LR ="GND", 右移: S1→S2→S3.....→S800; LR ="VCI", 左移: S1←S2←S3.....←S800; LR ="GND", Shift right: S1→S2→S3.....→S800; LR ="VCI", Shift left: S1←S2←S3.....←S800;
40	UD	垂直扫描方向控制 Vertical scan direction control. UD ="VCI", 底部 → 顶部; UD ="GND", 顶部 → 底部; UD ="VCI", Bottom → Top; UD ="GND", Top → Bottom;
41	VGH	TFT 栅极开启电压 TFT Gate ON Voltage
42	VGL	TFT 栅极关断电压 TFT Gate OFF Voltage
43	AVDD	模拟电源电压 Analog supply voltage
44	RESX	全局复位引脚。低电平进入复位状态 Global reset pin. Active Low to enter Reset State.
45	NC	不连接 Not connected
46	VCOM	TFT 共用电极电压 TFT Common Electrode Voltage
47	NC	不连接 Not connected
48	GND	电源地 Power supply ground
49~50	NC	不连接 Not connected
- 结束 - - END -		

3.2 接线说明 Wiring instructions

