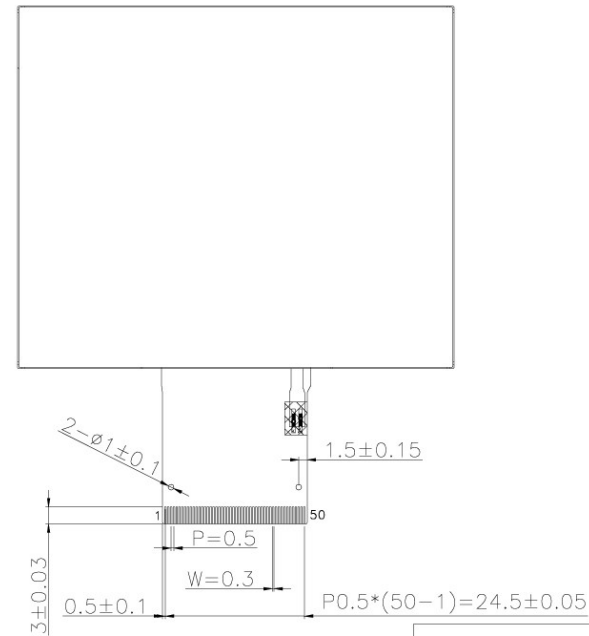
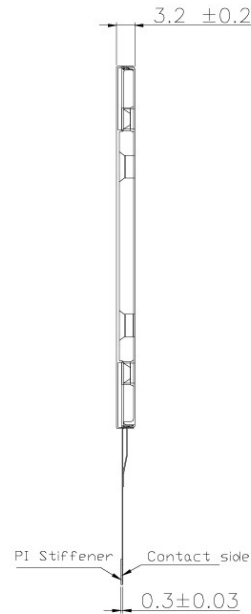
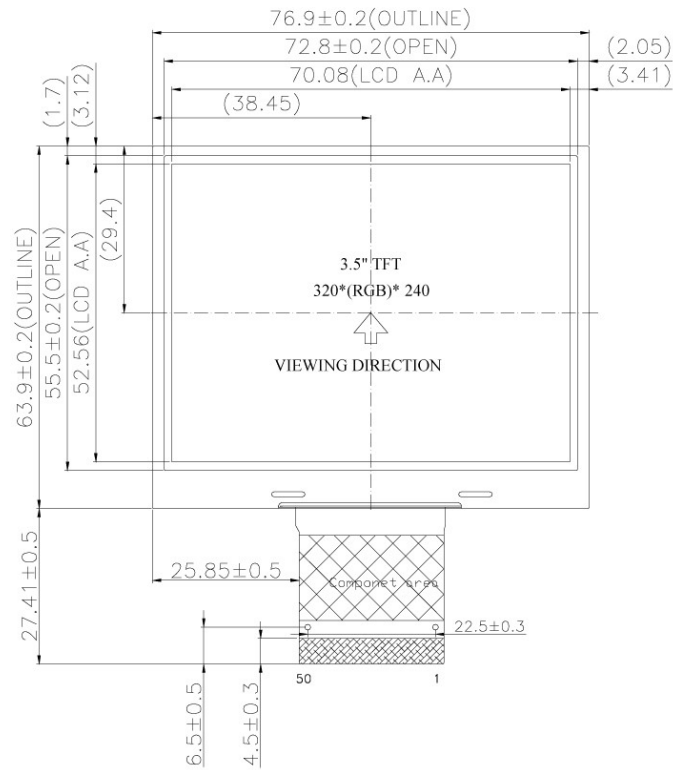


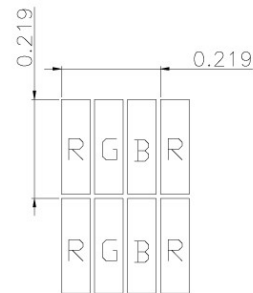


PIN	SYMBLE
1	VCI
2	VCI
3	VSS
4	VDDIO
5	VSS
6	RESET
7	DC/SDC(RS)
8	E/RD
9	WR
10	CS
11	SCL
12	SDO
13	SDI
14	WSYNC(NC)
15	D17
16	D16
17	D15
18	D14
19	D13
20	D12
36	VSYN
37	OE
38	VSS
39	PS0
40	PS1
41	PS2
42	PS3
43	VSS
44	NC
45	NC
46	NC
47	NC
48	VSS
49	LEDK
50	LEDA
21	D11
22	D10
23	D9
24	D8
25	D7
26	D6
27	D5
28	D4
29	D3
30	D2
31	D1
32	D0
33	VSS
34	DOTCLK
35	HSYN



LED CIRCUIT DIAGRAM

19. 2V@20mA



Pixtel Detail

Display Type	TFT
Optimum Viewing Direction	6 O'CLOCK
Upper Ploarizer Type	Anti-Glare
LCD Driver IC	SSD2119
Operating Voltage	VCI/VDDIO=3.3V
Operation Temperature	-20°C TO 70°C
Storage Temperature	-30°C TO 80°C
Interface	18 bit-RGB
Backlight	6-CHIP WHITE LED
Surface luminance	380 cd/m ² ? TYP.)
White X/Y	X:0.33±0.2 Y:0.35±0.2



东莞市精进光电有限公司

Jingjin Optoelectronics Co., Ltd.

NOTES:

- General Tolerance: ±0.2
- () reference dimension.
- Recommended Case Open Area Should Be Less Than Module V.A
- recommended cushion adherent area:TP V.A+1.6mm
- ROHS MUST BE COMPLIANT

DRAWN		UNIT	Customer NO.
		mm	
CHECKED	SCALE	NO.	Part NO.
	1:1	1/1	JTA351402301T1
APPROVED	DATE	VERSION	TITLE
		A	OUTLINE