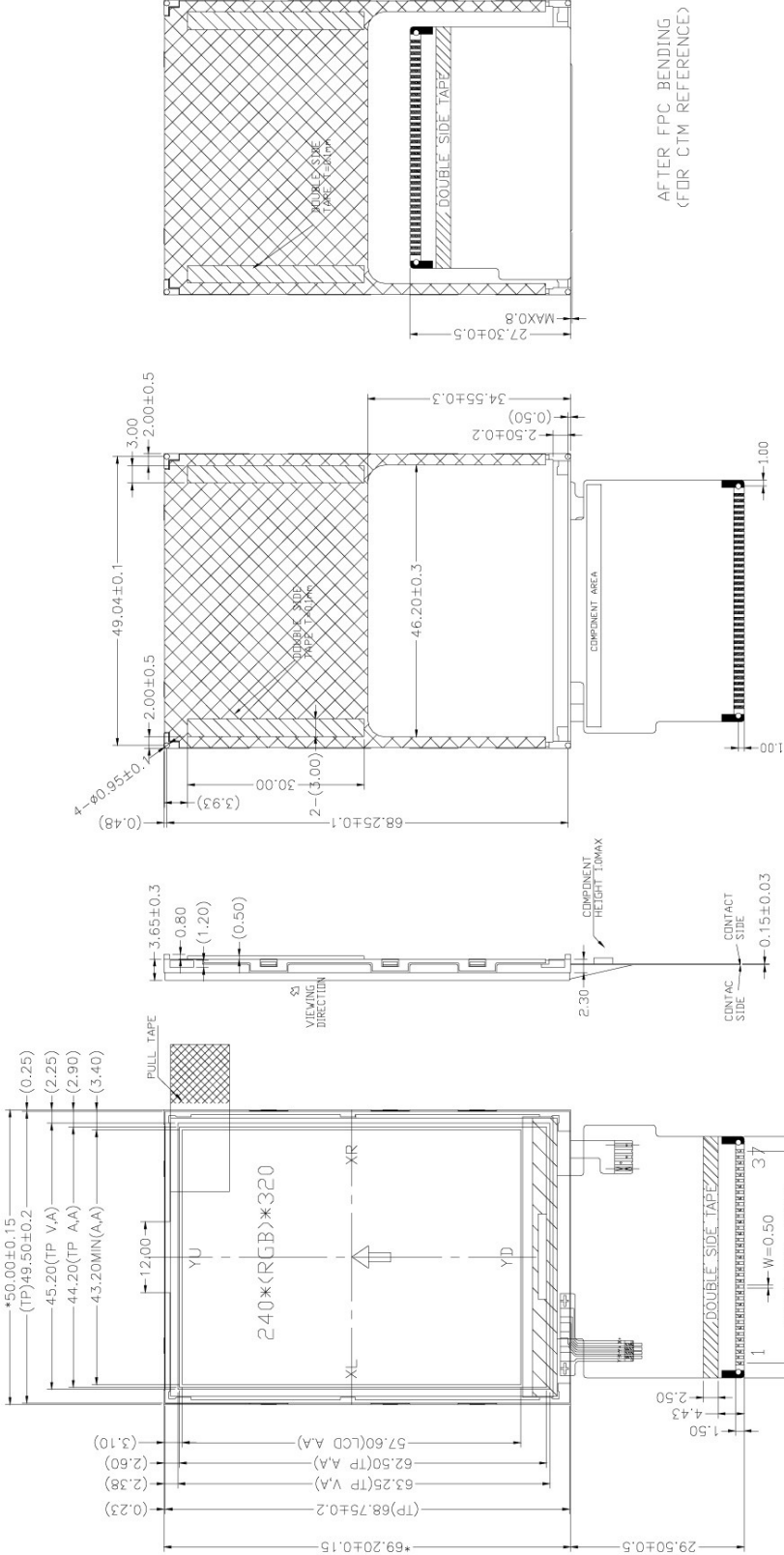
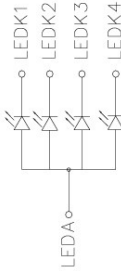
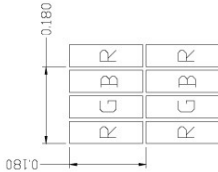


1	DB1
2	DB2
3	DB3
4	DB4
5	GND
6	VCC
7	CS
8	RS
9	WR
10	RD
11	IM0
12	X+
13	Y+
14	X-
15	Y-
16	LEDA
17	LEDK4
18	LEDK3
19	LEDK2
20	LEDK1
21	NC
22	DB5
23	DB10
24	DB11
25	DB12
26	DB13
27	DB14
28	DB15
29	DB16
30	DB17
31	RESET
32	VCC
33	VCC
34	GND
35	DB6
36	DB7
37	DB8



AFTER FPC BENDING
(FOR CTM REFERENCE)

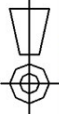


LED CIRCUIT DIAGRAM
3.2V@60mA



东莞市精进光电有限公司

Jingjin Optoelectronics Co., Ltd.

DRAWN		UNIT	Customer NO.
CHECKED	SCALE	NO.	Part NO.
APPROVED	DATE	VERSION	TITLE
		A	OUTLINE

1. General Tolerance: ±0.2

2. () reference dimension.

3. Recommended Case Open Area Should Be Less Than Module V.A

4. recommended cushion adherent area:TP V.A+1.6mm

5. ROHS MUST BE COMPLIANT