



Hunyi
准亿科技

Shenzhen Zhunyi Technology Co., Ltd.

LCM PRODUCT SPECIFICATION

1. General Specifications

Item	Main Display	Unit
Number of Pixels	240 * RGB * 320	Pixel
LCM Outline Dimension	37.68 (W) * 51.32 (H) * 2.3(T)	mm
LCD Outline Dimension	33.60 (W) * 46.60 (H) * 0.8(T)	mm
Active Area	30.60 * 40.80	mm
Pixel Pitch	0.0425 * 0.1275	mm
Display Mode	Normally Black	
Pixel Arrangement	RGB vertical stripe	
Viewing Direction	ALL	
Color Configuration	RGB	
LCD Transmittance	Typ:4.5%; Min:3.8%	
LCD Contrast Ratio	Typ:800; Min:640;	
FPC Version	Z20001-FLP V1	
Interface	4 lane SPI/8-bit MCU	
Operating Temperature	-20℃ ~ 70℃	
Storage Temperature	-30℃ ~ 80℃	
Backlight arrangement	White LED*3	
Luminance	-	
Luminance Uniformity	-	
Color chromaticity	-	
Weight	-	



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2. LCM-Pin Assignments ---80-8bit MCU

Pin No	Symbol	Description
1	GND	Ground
2	VDD	Analog Power (2.8V)
3	IOVDD	Interface Power (1.8V)
4	LEDA	LED Anode
5	LEDK	LED Cathode
6	LEDK	LED Cathode
7	LEDK	LED Cathode
8	TE	Tearing effect signal is used to synchronize MCU to frame memory writing
9	GND	Ground
10	RD	Read enable in 8080 MCU parallel interface
11	WR	Read enable in 8080 MCU parallel interface
12	DC	Display data/command selection pin in parallel interface
13	CS	Chip selection pin
14	GND	Ground
15	D7	MCU parallel interface data bus. -If not used, please fix this pin at VDDI or DGND.
16	D6	MCU parallel interface data bus. -If not used, please fix this pin at VDDI or DGND.
17	D5	MCU parallel interface data bus. -If not used, please fix this pin at VDDI or DGND.
18	D4	MCU parallel interface data bus. -If not used, please fix this pin at VDDI or DGND.
19	D3	MCU parallel interface data bus. -If not used, please fix this pin at VDDI or DGND.
20	D2	MCU parallel interface data bus. -If not used, please fix this pin at VDDI or DGND.
21	D1	MCU parallel interface data bus. -If not used, please fix this pin at VDDI or DGND.
22	D0	MCU parallel interface data bus. -If not used, please fix this pin at VDDI or DGND.
23	RES	Reset signal (1.8V)
24	GND	Ground
25	GND	Ground



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LCM-Pin Assignments----4 lane SPI

Pin No	Symbol	Description
1	GND	Ground
2	VDD	Analog Power (2.8V)
3	IOVDD	Interface Power (1.8V)
4	LEDA	LED Anode
5	LEDK	LED Cathode
6	LEDK	LED Cathode
7	LEDK	LED Cathode
8	TE	Tearing effect signal is used to synchronize MCU to frame memory writing
9	GND	Ground
10	GND	Ground
11	WR	Read enable in 8080 MCU parallel interface
12	DC	Display data/command selection pin in parallel interface
13	CS	Chip selection pin
14	SDA	SPI interface input/output pin
15	GND	Ground
16	GND	Ground
17	GND	Ground
18	GND	Ground
19	GND	Ground
20	GND	Ground
21	GND	Ground
22	GND	Ground
23	RES	Reset signal
24	IOVDD	Interface Power (1.8V)
25	IOVDD	Interface Power (1.8V)

