



ZITROK ELECTRONICS CO., LIMITED

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Capability List for Rigid PCBs

Item	Production Capacity
Layer Count	1-24L
Material	(Normal/High Tg)FR-4,CEM-3, PTFE Aluminum Base, Rogers, etc
Maximum Panel Size	500mm*800mm(2layer),400mm*550mm(multi-layer)
Finished Board Thickness	0.20mm~3.2mm(2layer),0.6mm~5.0mm(multi-layer)
Finished Outer Copper Thickness	35um-190um
Finished Inner Copper Thickness	17um-190um
Minimum Trace Width/Space	0.075mm
Minimum Insulation Layer Thickness	0.1mm
Minimum Finished Hole Size(Mechanical)	0.15mm
Minimum Hole Position Tolerance	±0.075mm
Hole Size Tolerance	±0.075mm (PTH)
	±0.05mm (NPTH)
Outline Dimension Tolerance	±0.13mm
Conductor Width Tolerance	±0.05mm or ±20% of Original Artwork
Warp & Twist Ratio	0.70%
Insulation Resistance For E/T test	10K Ω-20M Ω
Conductivity Resistance For E/T test	<50 Ω
Voltage For E/T test	300v maximum
Minimum Spacing Between hole edge and Circuit Pattern	0.2mm(PTH)
	0.13mm(NPTH)
Registration Tolerance Front/Back Image	0.075mm
Layer to Layer Registration	0.075mm
Board Thickness Tolerance	±8%(t≥1.0mm)
	±10%(t<1.0mm)
Aspect Ratio	8:1
Plugging hole Diameter with solder mask	0.20mm-0.60mm
Minimum Solder dam Width	0.1mm
Minimum Solder Mask Clearance	0.05mm
Solder Mask Type/Silkscreen Ink	LPI(green, white, black ,red ,etc)
Impedance Control tolerance	±10%
Minimum Board thickness for V-Groove process	0.6mm
Remained Board thickness for V-Groove process	0.3mm
Thickness Tolerance for V-Groove process	±0.1mm
Minimum Spacing From V-Groove to	0.4mm
Slot Dimension Tolerance(Length/Width),Length>=2*Width	±0.125mm/±0.1mm
Au thickness	0.0762um-0.762um(Hard Au)
	0.0254um-0.127um(Soft Au)
Ni thickness	2.032-7.362um



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Capability List for FPC

Item	Production Capacity
Layer Count	1-6
Material	1 Layer:0.07mm(Min.)
	2 Layer: 0.125mm(Min.)
	4 Layer: 0.34mm(Min.)
	5 Layer: 0.42mm(Min.)
	6 Layer: 0.51mm(Min.)
Minimum Trace Width/Space	1 Layer: 2/2mils
	2 Layer up: 3/3mils
Surface finishing	ENIG,OSP,immersiion tin,gold plating
Stiffening material	PI, FR4/PC,AL,SUS,PET
Bond Maerial	PSA, Thermo-setting,Conductive(PSA type only)
Outline Dimension Tolerance	L<100mm: +/-0.1mm
	100mm<L<200mm: +/-0.15mm
	200mm<L<400mm: +/-0.50mm
Conductor Width Tolerance	0.1mm<W<0.25mm: +/-0.03mm
	0.25mm<W<0.5mm: +/-0.05mm
	0.5mm<W: +/-0.13mm
Hole tolerance	0.2mm<D<0.8mm: +/-0.05mm
	0.8mm<D<1.6mm: +/-0.08mm
	1.6mm<D<6.25mm: +/-0.10mm
PIN PAD tolerance	P<25, +/-0.05mm
	25<P<50, +/-0.08mm
	50<P<100, +/-0.12mm
	100<P<150, +/-0.16mm
	150<P<200, +/-0.25mm