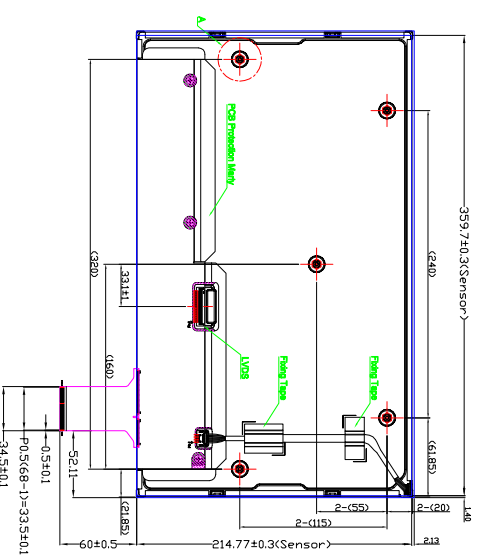
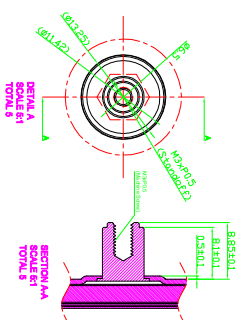




1	GND	31	NC
2	NC	32	NC
3	VCC	33	EN
4	VCC	34	FAULT
5	VCC	35	SCL
6	NC	36	SDA
7	GND	37	WPN
8	GND	38	GND
9	GND	39	GND
10	ORXIN0-	40	NC
11	ORXIN0+	41	LEDA
12	ORXIN1-	42	LEDA
13	ORXIN1+	43	LEDA
14	ORXIN2-	44	NC
15	ORXIN2+	45	LEDK
16	ORXCLKIN-	46	LEDK
17	ORXCLKIN+	47	LEDK
18	ORXIN3-	48	LEDK
19	ORXIN3+	49	NTC_A
20	EFXIN0-	50	NTC_K

7.UNMARKED TOLERANCE:±0.3



NO.	Pin Name
1	VDD33
2	SDA
3	SCL
4	INT
5	RST
6	GND



XINSUN DISPLAY INTEGRATION