

MAXEN ELECTRONICS LIMITED

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SPECIFICATION

MX-B90HL60-IS07

For Customer's Acceptance:

Approved By	Comment		
PREPARED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DEPT

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2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2021.09.23	A	ALL	The first release	DH

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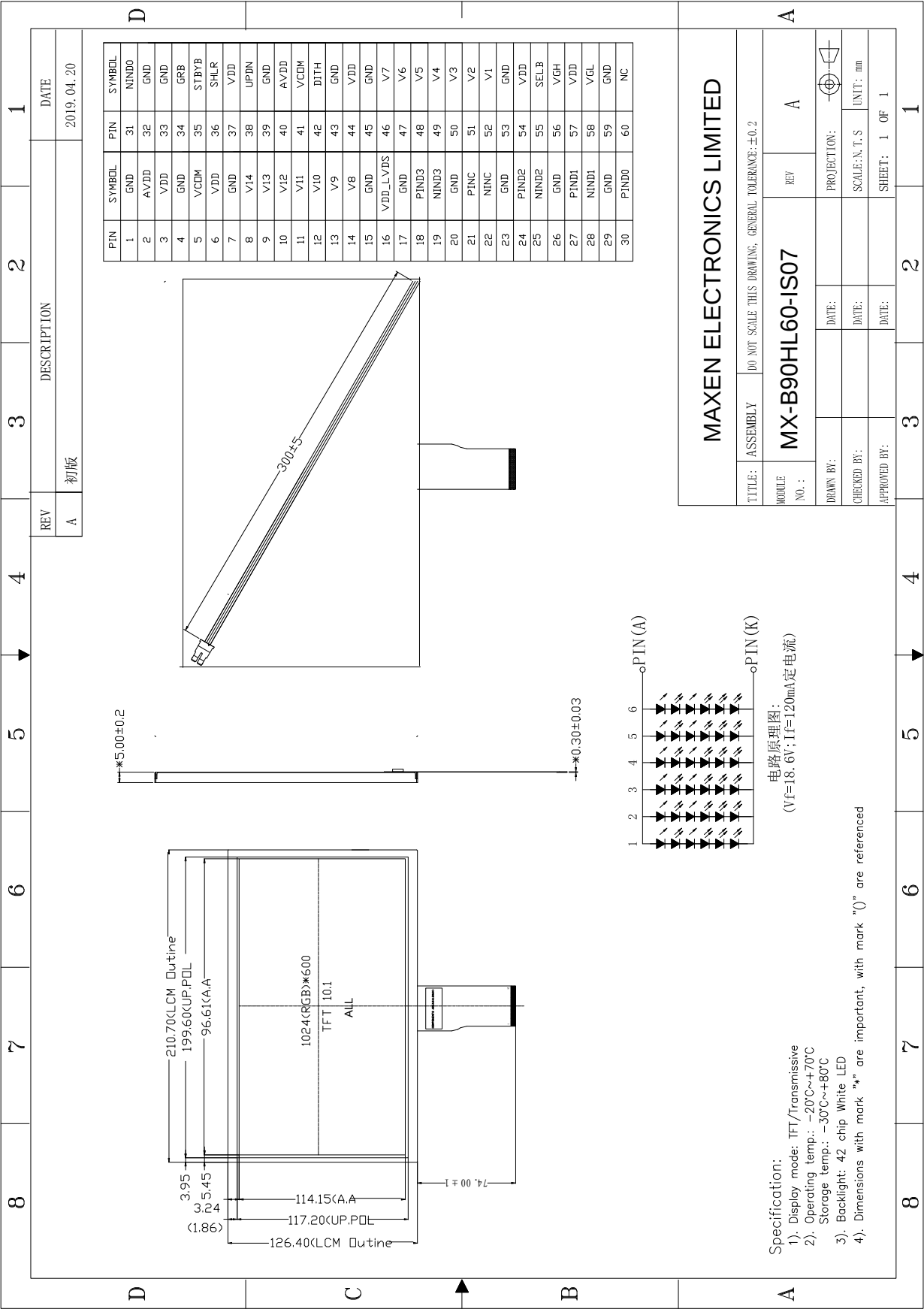
3. General Specifications

MX-B90HL60-IS07 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The **9.0"** display area contains **1024(RGB) x600** pixels and can display up to **16.7M** colors. This product accords with ROHS environmental criterion.

Item	Contents	Unit	Note
LCD Type	a-Si TFT	-	
Display color	16.7M		1
Viewing Direction	ALL	O'clock	
Gray Scale Inversion	FREE	O'clock	
Pixel Configuration	RGB Vertical Stripe		
Pixel Pitch	0.192x0.19025	mm	
Display Mode	Transmissive Normally Black		
Resolution	1024×RGB×600	dots	
Module size	210.7*124.6*5.0	mm	2
Active Area(W×H)	196.608*114.15	mm	
Backlight	36-LEDs (white)	pcs	
Brightness(LCM)	500	cd/m ²	TYP
Interface	LVDS	-	
Driver IC	HX8282+HX8696	-	

Note 1: Color tune is slightly changed by temperature and driving voltage.

4.Outline.Drawing



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5. Absolute Maximum Ratings(Ta=25°C)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25°C)

Parameter	Symbol	Min	Typ	Max	Unit	Note
Power supply	VDD	-0.3	-	3.96	V	
	AVDD	-0.5	-	14.85	V	
	VGL	-20	-	0.3	V	
	VGH	-0.3		42	V	
Operating Temperature	TOP	-20	-	70	°C	
Storage Temperature	TST	-30	-	80	°C	

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
2. $V_{CC} > V_{SS}$ must be maintained.
3. Please be sure users are grounded when handing LCD Module

5.2 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30°C	80°C	-20°C	70°C	1,2

1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

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6. Electrical Specifications and Timing Chart

6.1.1 LCM Electrical characteristics(V_{ss}=0V ,Ta=25°C)

Parameter		Symbol	Min	Typ	Max	Unit	Note
Power supply		VDD	3.0	3.3	3.6	V	
		AVDD	9.6	10.2	13.5	V	
		VGL		-8		V	
		VGH		18		V	
Input signal voltage		VCOM	4.0	4.4	4.6		
Input voltage	'H'	V _{IH}	0.7V _{VDD}	-	V _{VDD}	V	
	'L'	V _{IL}	0	-	0.3V _{VDD}	V	

5.3 LED backlight specification(V_{SS}=0V ,Ta=25°C)

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage	V _f	-	16.8	18.6	20.4	V	1
Supply current	I _f	-	-	120	-	mA	2
Number of LED	-	6Sx6P	-	36	-	Piece	
Power Consumption	-	-	2016	2232	2448	mW	
LED life time	-	-	30000	-	-	Hrs	

Note:1: V_{LED}=V_{LED}(+)-V_{LED}(-).

2:The current of LED is 20mA.

A LED drive in constant current mode is recommended.

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6.3 Interface signals

Pin NO.	Symbol	Description
1	GND	Ground
2	AVDD	Analog Power
3	VDD	Digital Power
4	GND	Ground
5	VCOM	Common voltage
6	VDD	Digital Power
7	GND	Ground
8~14	V14~V8	Gamma correction voltage reference
15	GND	Ground
16	VDD_LVDS	LVDS Power
17	GND	Ground
18	PIND3	Positive LVDS Differential Data Inputs
19	NIND3	Negative LVDS Differential Data Inputs
20	GND	Ground
21	PINC	Positive LVDS Differential Clock Inputs
22	NINC	Negative LVDS Differential Clock Inputs
23	GND	Ground
24	PIND2	Positive LVDS Differential Data Inputs
25	NIND2	Negative LVDS Differential Data Inputs
26	GND	Ground
27	PIND1	Positive LVDS Differential Data Inputs
28	NIND1	Negative LVDS Differential Data Inputs
29	GND	Ground
30	PIND0	Positive LVDS Differential Data Inputs
31	NIND0	Negative LVDS Differential Data Inputs

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32	GND	Ground
33	GND	Ground
34	GRB	Global reset pin. Active Low to enter Reset State. Normally pull high. It' s necessary to connecting with an RC delay circuit for stability. (GRB delay VDD larger than 1ms)
35	STBYB	Standby mode, normally pull high STBYB=" 1 " , Normal Operation STBYB=" 0 " ,Timing control, source driver will turn off, all output are high-Z
36	SHLR	Horizontal Inversion
37	VDD	Digital Power
38	UPDN	Vertical Inversion
39	GND	Ground
40	AVDD	Analog Power
41	VCOM	Common voltage
42	DITH	Dithering function
43	GND	Ground
44	VDD	Digital Power
45	GND	Ground
46~52	V7~V1	Gamma correction voltage reference
53	GND	Ground
54	VDD	Digital Power
55	SELB	6 bit/8 bit mode select
56	VGH	Positive Power for TFT
57	VDD	Digital Power
58	VGL	Negative Power for TFT
59	GND	Ground
60	NC	No connection

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7. Optical Characteristics

Item	Symbol		Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp		$\theta=0^{\circ}$ $\Phi=0^{\circ}$	-	500	-	cd/m ²	1
Uniformity	△Bp			75	80	-	%	1,2
Viewing Angle	3:00		Cr≥10	-	80	-	Deg	3
	6:00			-	80	-		
	9:00			-	80	-		
	12:00			-	80	-		
Contrast Ratio	Cr		$\theta=0^{\circ}$ $\Phi=0^{\circ}$	-	800		-	4
Response Time	T _r +T _f				30	40	ms	5
Color of CIE Coordinate	W	x	$\theta=0^{\circ}$ $\Phi=0^{\circ}$	-0.04	0.30	+0.04	-	1,6
		y			0.33		-	
	R	x			0.619		-	
		y			0.337		-	
	G	x			0.279		-	
		y			0.538		-	
	B	x			0.141		-	
		y			0.134		-	
Color Gamut				-	50	-	%	

Note: The parameter is slightly changed by temperature, driving voltage and materiel

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white.

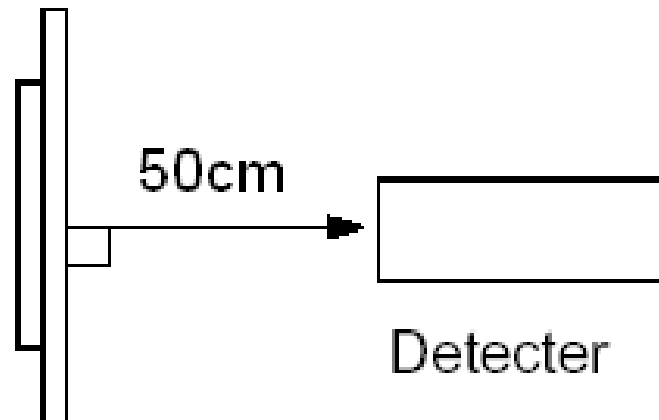
The brightness is the average value of 9 measured spots. Measurement equipment PR-705 ($\Phi 8\text{mm}$)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: $T_a=25^\circ\text{C}$.

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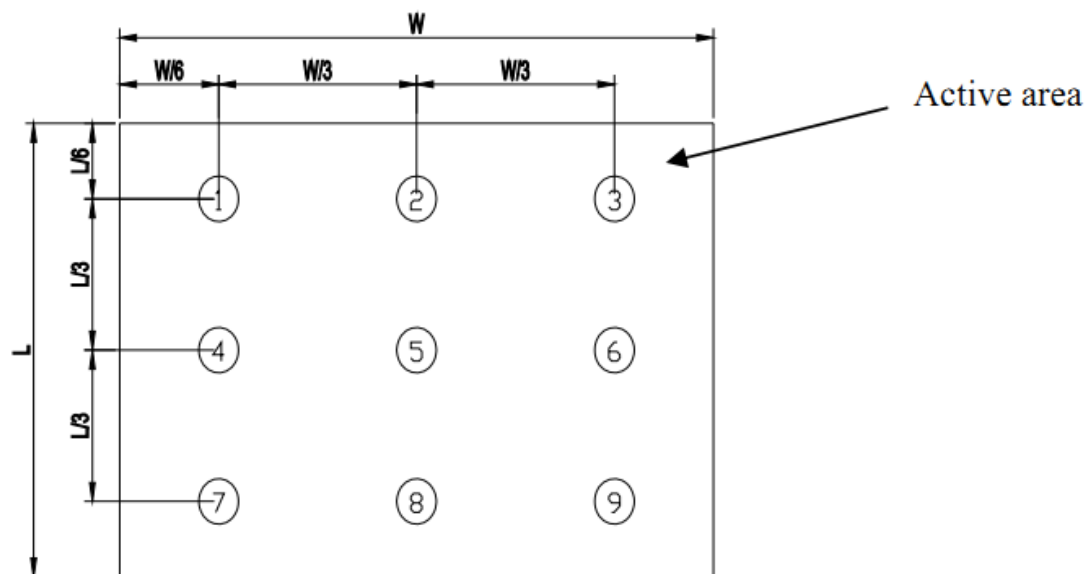


Note 2: The luminance uniformity is calculated by using following formula.

$$\Delta B_p = B_p (\text{Min.}) / B_p (\text{Max.}) \times 100 (\%)$$

$B_p (\text{Max.})$ = Maximum brightness in 9 measured spots

$B_p (\text{Min.})$ = Minimum brightness in 9 measured spots.

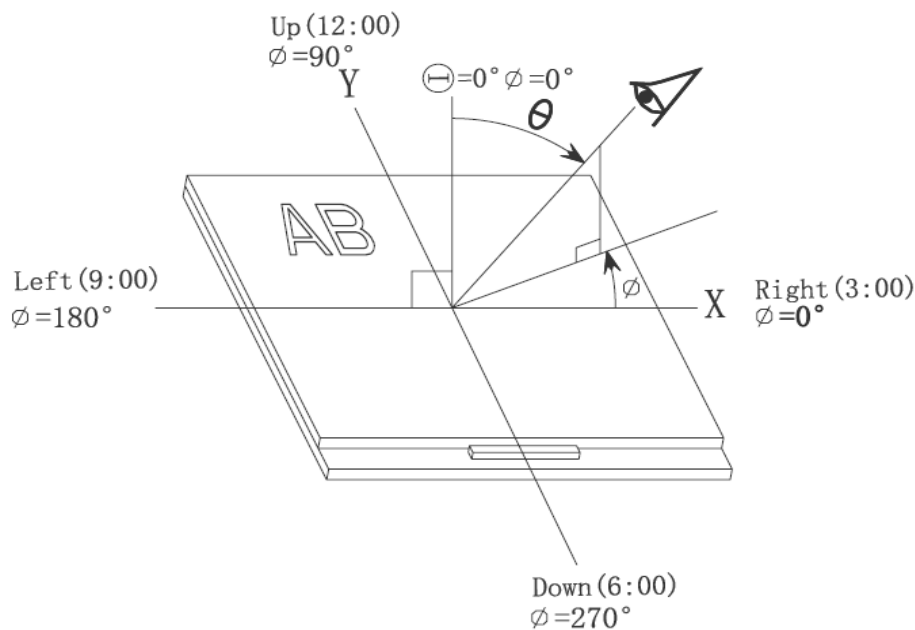


Note 3: The definition of viewing angle:

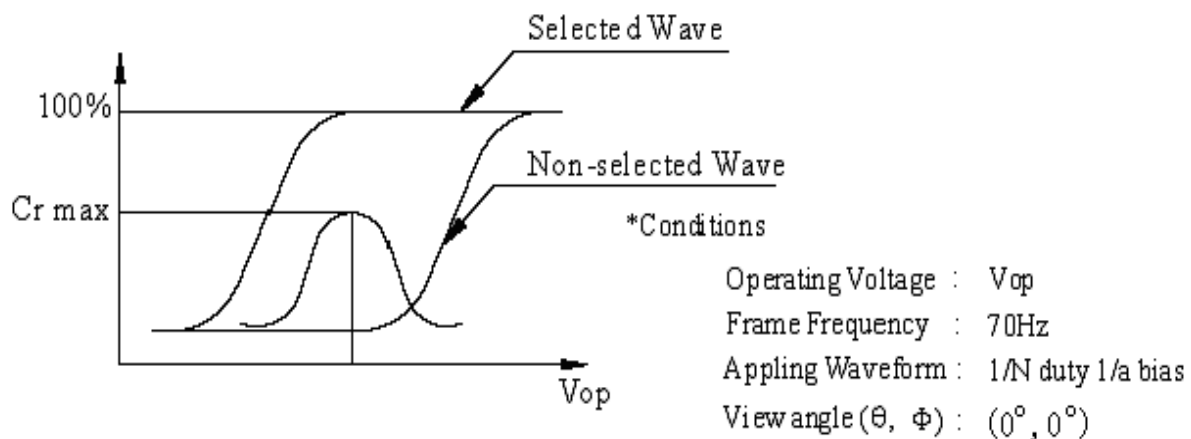
Refer to the graph below marked by θ and ϕ

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Note 4: Definition of contrast ratio.(Test LCD using DMS501)



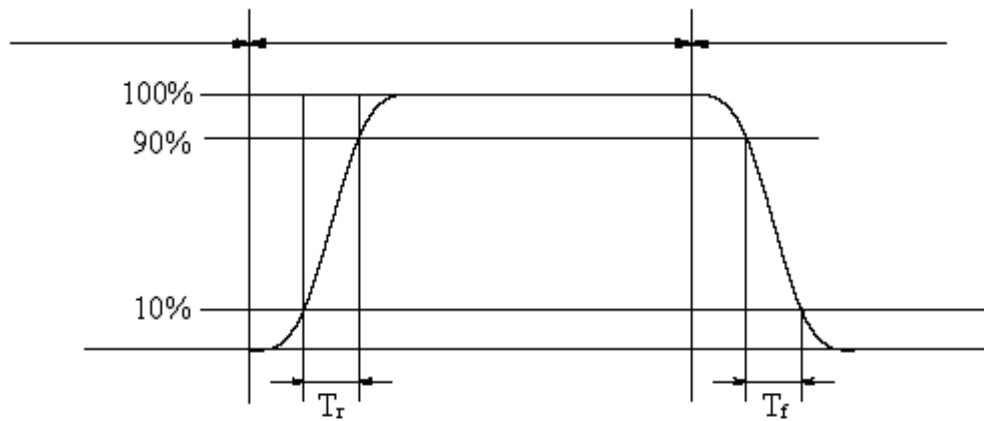
$$\text{Contrast ratio}(Cr) = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$

Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.

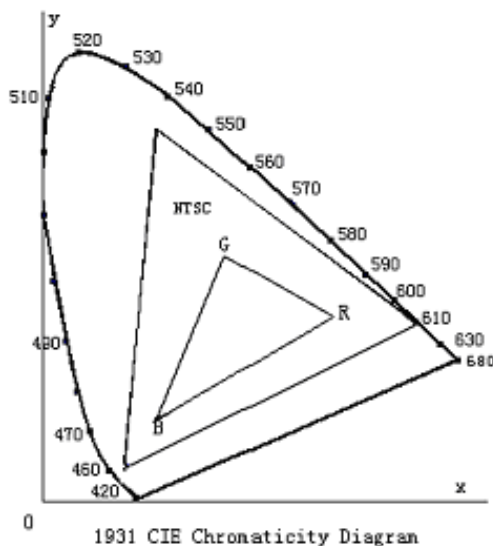
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The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

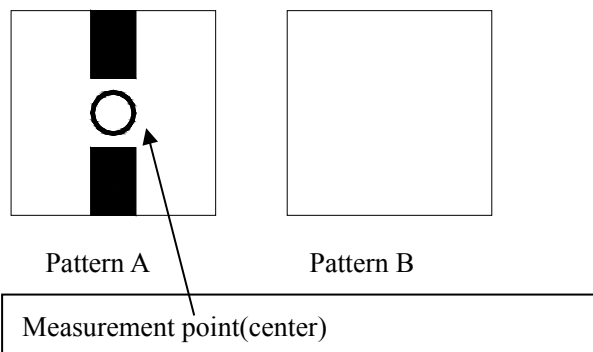


Color gamut:

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 7: Definition of cross talk.

Cross talk ratio(%)=| pattern A Brightness-pattern B Brightness | /pattern A Brightness*100

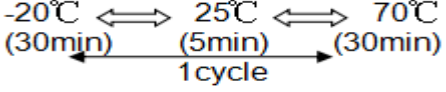


Electric volume value=3F+/-3Hex

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8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80°C±2°C 72H Restore 2H at 25°C Power off	1. After testing, cosmetic and electrical defects should not happen. 2. Total current consumption should not be more than twice of initial value.
2	Low Temperature Storage	-30°C±2°C 72H Restore 2H at 25°C Power off	
3	High Temperature Operation	70°C±2°C 72H Restore 2H at 25°C Power on	
4	Low Temperature Operation	-20°C±2°C 72H Restore 4H at 25°C Power on	
5	High Temperature/Humidity Operation	50°C±2°C 90%RH 72H Power on	
6	Temperature Cycle(Storage)	 after 5 cycle, Restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s ² , 120min	Not allowed cosmetic and electrical defects.
8	Shock Test	Half- sine wave, 300m/s ² , 11ms	
9	ESD Test	Air discharge: ±8KV, Contact discharge: ±4KV	

Note: Operation: Supply 3.3V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

9 Quality level

9.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially

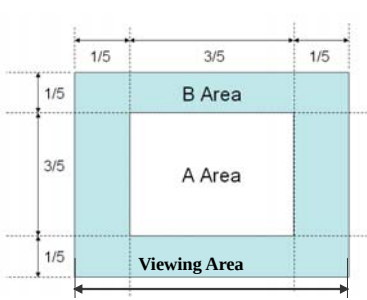
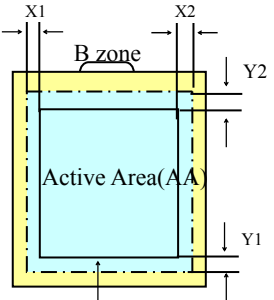
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degrade usability for product applications, including all functional defects(such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

9.2 Definition of inspection range

For dot defect of TFT LCD which is not smaller than 3 inches, dividing three areas to make a judgment (according to figure 1). A area : center of viewing area B area : periphery of viewing area C area : Outside viewing area For other defects, dividing two areas to make a judgment (according figure 2). A zone : Inside Viewing area B zone : Outside Viewing area X1(A.A~V.A): 2mm X2(A.A~V.A): 2mm Y1(A.A~V.A): 2mm Y2(A.A~V.A): 2mm	 Figure 1  Figure 2
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9.3 Inspection items and general notes

General notes	1.Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and Maxen. 2.Viewing area should be the area which Maxen guarantees. 3.Limit sample should be prior to this Inspection standard. 4.Viewing judgment should be under static pattern. 5.Inspection conditions Inspection distance: 250 mm (from the sample) Temperature : 25±5 °C Inspection angle : 45 degrees in 6 o'clock direction (all defects in viewing area should be inspected from this direction)	
Inspection items	Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble	The color of a small area is different from the remainder. The phenomenon doesn't change with voltage
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage
	Polarizer defect	Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass
	Dot defect (TFT LCD)	The pixel appears bright or dark abnormally when display
	Functional defect	No display, Abnormal display, Open or missing segment, Short circuit, False viewing direction

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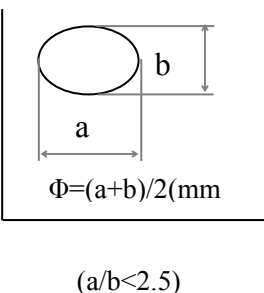
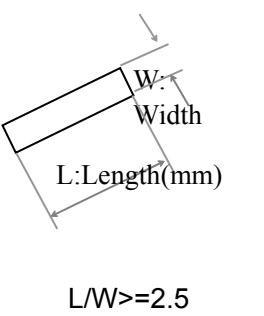
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	Glass defect	Glass crack, Shaved corner of glass, Surplus glass
	PCB defect	Components assembly defect

9.4 Outgoing Inspection level

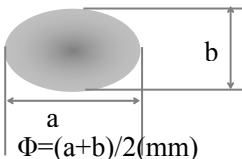
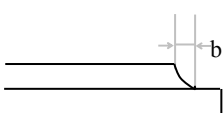
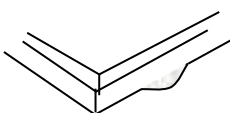
Outgoing Inspection standard	Inspection conditions	Inspection				
		Min.	Max.	Unit	IL	AQL
Major Defects	See 8.3 general notes	See 8.5			II	0.065
Minor Defects	See 8.3 general notes	See 8.5			II	0.065
Note: Sampling standard conforms to GB2828						

9.5 Inspection Items and Criteria

Inspection items			Judgment standard			
			Category		Acceptable number	
					A zone	B zone
1	Black spot, White spot, Pinhole, Foreign Particle, Particle in or on glass, Scratch on glass		A	$\Phi \leq 0.20$	Neglected	Neglected
			B	$0.20 < \Phi \leq 0.25$	3	Neglected
			C	$0.25 < \Phi \leq 0.3$	2	Neglected
			D	$0.3 < \Phi \leq 0.4$	1	3
			E	$0.4 < \Phi \leq 0.5$	0	2
			Total defective point(B,C)		1	-
2	Black line, White line, and Particle Between Polarizer and glass, Scratch on glass		A	$W \leq 0.03$	Neglected	Neglected
			B	$0.03 < W \leq 0.05$ $L \leq 3.0$	3	Neglected
			C	$0.05 < W \leq 0.1$ $L \leq 3.0$	2	Neglected
			D	$0.05 < W \leq 0.1$ $L \leq 4.0$	1	3
			E	$W > 0.1$ $L > 4.0$	0	2
			Total defective point(B,C)		1	-
3	Bright spot		any size		none	none
4	Contrast		A	$\Phi < 0.2$	Neglected	Neglected

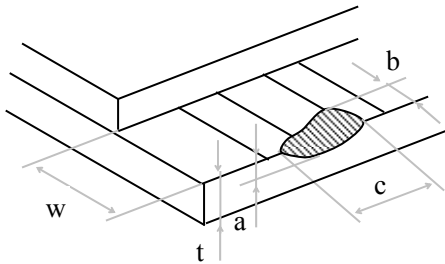
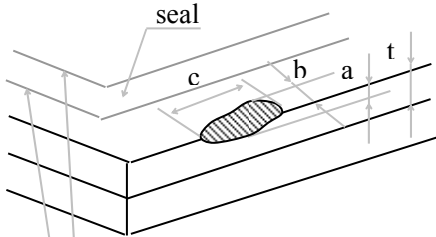
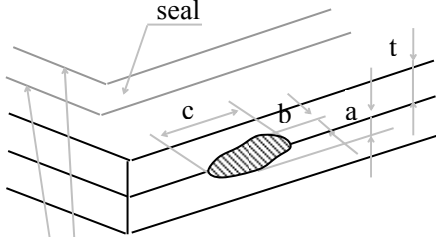
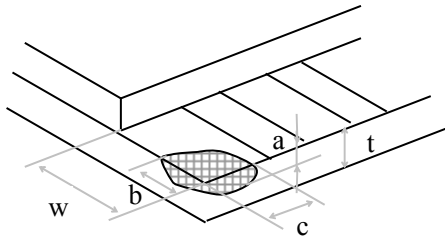
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	variation		B	$0.2<\Phi\leq0.3$	2	
			C	$0.3<\Phi\leq0.4$	1	
			D	$0.4<\Phi$	0	
			Total defective point(B,C)		3	
5	Bubble inside cell		any size		none	none
6	Polarizer defect (if Polarizer is used)	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Refer to item 1 and item 2.			
		Bubble, dent and convex	A	$\Phi\leq0.1$	Neglected	Neglected
			B	$0.1<\Phi\leq0.2$	2	Neglected
			C	$0.2<\Phi\leq0.3$	1	2
7	Surplus glass	Stage surplus glass 	$B\leq0.3\text{mm}$			
		Surrounding surplus glass 	Should not influence outline dimension and assembling.			
8	Open segment or open common		Not permitted			
9	Short circuit		Not permitted			
10	False viewing direction		Not permitted			
11	Contrast ratio uneven		According to the limit specimen			
12	Crosstalk		According to the limit specimen			
13	Black /White spot(display)		Refer to item 1			
14	Black /White line(display)		Refer to item 2			

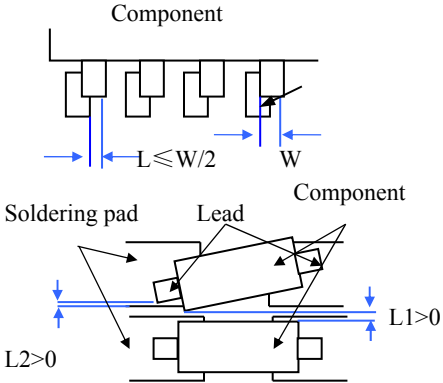
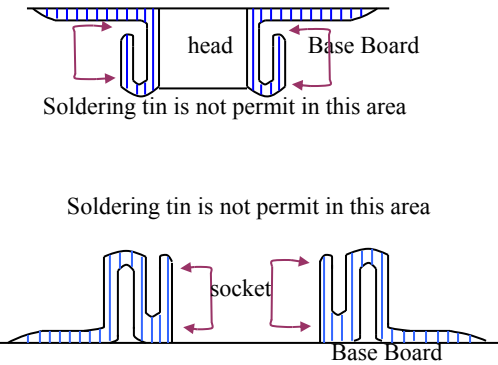
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Inspection items			Judgment standard		Acceptable number
			Category(application: B zone)		
15	Glass defect crack	i) The front of lead terminals	A	$a \leq t, \quad b \leq 1/5W, \quad c \leq 3\text{mm}$	Max.3 defects allowed
			B	Crack at two sides of lead terminals should not cover patterns and alignment mark	
		ii) Surrounding crack-non-contact side			
		 <u>Inner border line of the seal</u> <u>Outer border line of the seal</u>	$b < \text{Inner borderline of the seal}$		
		iii) Surrounding crack- contact side			
		 <u>Inner border line of the seal</u> <u>Outer border line of the seal</u>	$b < \text{Outer borderline of the seal}$		
		iv) Corner	A	$a \leq t, \quad b \leq 3.0, \quad c \leq 3.0$	
			B	Glass crack should not cover patterns u and alignment mark and patterns.	

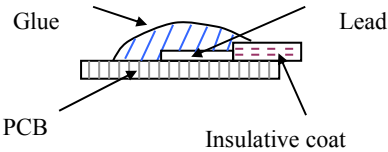
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Inspection items			Judgment standard
			Category(application: B zone)
16	PCB defect	Component soldering: No cold soldering、short、open circuit、burr、tin ball The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1); the sheet component deviation: Pin deviates from the pad and contact with the near components is not permitted (Pic.2)	
		lead defect: The lead lack must be less than 1/3 of its width; The lead burr must be less than 1/3 of the seam; Impurities connect with the near leads is not permitted	
		Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted	

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	Glue on root of the speaker receiver and motor lead: The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.	 The diagram illustrates the correct application of glue when attaching a lead to a PCB. It shows a cross-section of the assembly. A lead with an insulative coat is being joined to a PCB. The glue is applied to the root of the lead and the PCB, ensuring that the glue envelops the insulative coat of the lead. Labels include: Glue, Lead, PCB, and Insulative coat.
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10. Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

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10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- a. Be sure to ground the body when handling the LCD Modules.
- b. Tools required for assembly, such as soldering irons, must be properly ground.
- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range.
If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: $\leq 80\%$

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.