



PRODUCT GROUP

REV

Rev.01

TFT-LCD PRODUCT

Ver.P0

2026/05/09

DCE156FH0-B01-1000 规格书
SPECIFICATION

承 认 书 Approved by	
审核：	确认：

客户确认结果：

Customer
客 户： _____

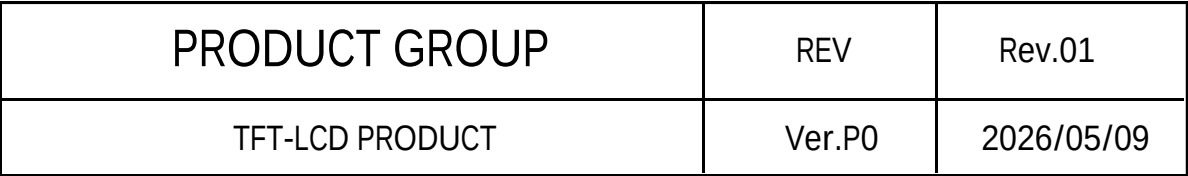
Product
品 名： _____ 15.6 inch LCD 模组

Part NO.
产品型号： _____ DCE156FH0-B01-1000

DATE
日 期： _____ 2026/05/09

Approved 核 准	Checked 审 核	Prepared 制 作

[illegible]

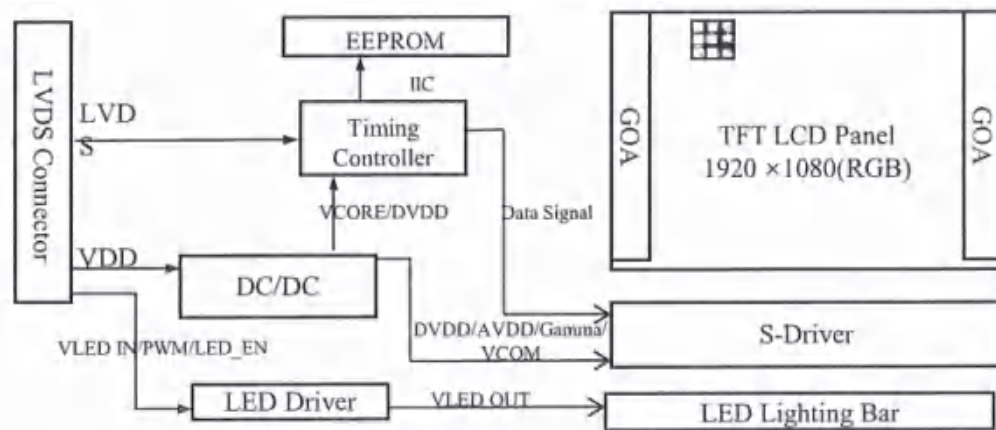
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1.0 General Description

1.1 Introduction

DCE156FH0-B01-1000 is a color active matrix TFT LCD Panel using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 15.6 inch diagonally measured active area with FHD resolutions(1920 horizontal by 1080 vertical pixel array).Each pixel is divided into RED, GREEN,BLUE dots which are arranged in vertical stripe and this Panel can display 16.7M colors。



1.2. Features

- TFT Type: a-si
- Display Mode: ADS-Prc
- color depth:16.7M colors
- Interface: LVDSInterface with 2 pixel/ clock
- Mode: DE(Data Enable) only
- Reverse Type: Column flipping
- RoHS compliant

1.3. General information

Item	Specification	Unit
Outline Dimension(LCM)	359.26±0.3x211.53±0.3x6.2/8.7±0.3(未包函下铁凸包点尺寸)	mm
Display area(LCM)	344.16*193.59	mm
Number of Pixel	1920(H) x 1080 (V)	pixels
Pixel pitch	0.17925x0.17925	mm
Pixel Arrangement	RGB Vertical stripe	
Display mode	Normal Black	
Display Colors	16.7	M
Refresh Rate	60	Hz
Luminous(LCM)	850(Min.)/1000(Typ.)	Cd/m ²
Back Light	White LED	
Weight	TBD	g

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2.0 ABSOLUTE MAXIMUM RATINGS

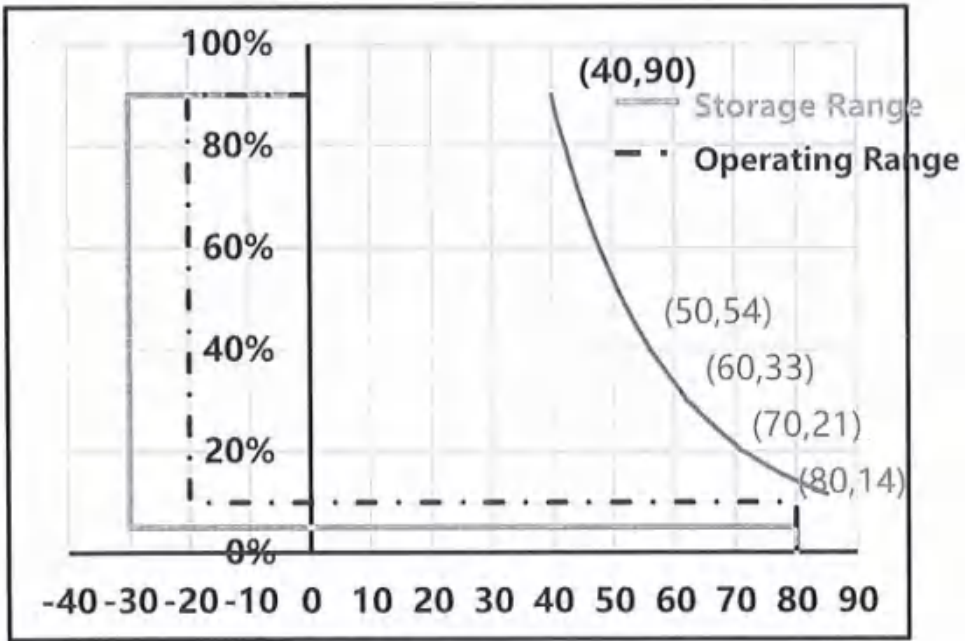
The followings are maximum values which,if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

<Table 2. Absolute Maximum Ratings>

Parameter		Symbol	Min	Max	Unit	Remarks
LCD	Panel Input Voltage	LCD_VCC	2.5	5	V	Ta =25 °C
Environment	Operating Temperature	TOP	-20	70	°C	Note:1
	Storage Temperature	TST	-30	80	°C	
	Operating Ambient Humidity	Hop	10	90	%RH	
	Storage Humidity	Hst	5	90	%RH	

Note : 1) Temperature and relative humidity range are shown in the figure below.

<Figure 2. RA Condition>



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3.0 ELECTRICAL SPECIFICATIONS

3.1 TFT LCD Module

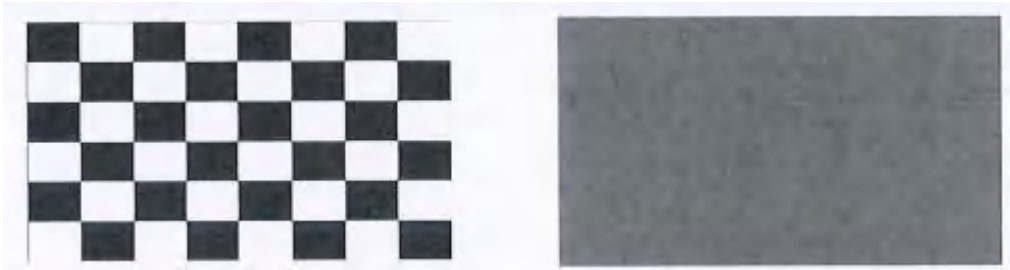
< Table 3.LCD FOB/FOG Electrical Specifications >

[Ta =25±2 °C]

Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
Power Supply Voltage		VDD	3.0	3.3	3.6	V	Note 1
Power Supply Ripple Voltage		VRP	-	-	300	mV	
Power Supply Current		IDD	-	180	590	mA	
Rush current		IRUSH	-	-	2	A	Note 2
LVDS Interface	Differential Input High Threshold Voltage	VLVTH	-	-	+100	mV	
	Differential Input Low Threshold Voltage	VLVTL	-100	-	-	mV	
	Input Differential Voltage	VID	100	400	600	mV	
	Common Input Voltage	VLVC	0.6	1.2	$1.6 - V_{ID} / 2$	V	

Notes : 1. The supply voltage is measured and specified at the interface connector of FOB
The current draw and power consumption specified is for VDD=3.3V, Frame rate fy=60Hz
and Clock frequency =75.3MHz. Test Pattern of power supply current

a)Typ :Mosaic 8 x6 Pattern(L0/L255) b)Max:Ver subline L255



2. The duration of rush current is about 2ms and rising time of Power Input is 1ms(min)

3.0 ELECTRICAL SPECIFICATIONS

3.2 Back-light Unit

< Table 4. LED driver Electrical Specifications >

[Ta =25±2°C]

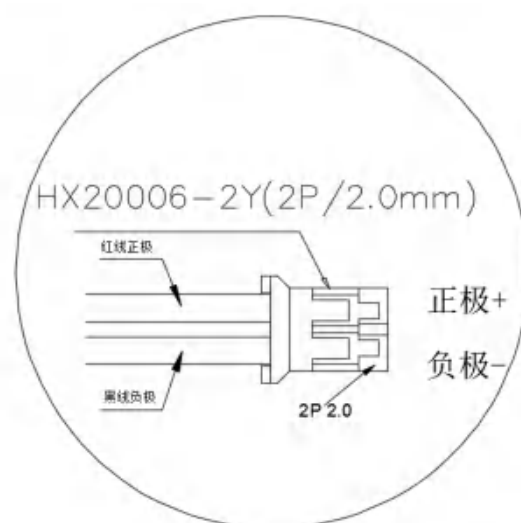
PARAMETER	Sym.	Min.	Typ.	Max.	Unit	Test Condition	Note
LED Current	IF	—	420	—	mA	—	—
LED Voltage	VF	30.8	33.0	37.4	V	I=420mA	—
Luminous	IV	850	1000	—	Cd/m ²	I=420mA	—
Life Time		30000	—	—	Hr.	I=420mA	—
Color	White						

Note (1) Permanent damage may occur to the LCD module if beyond this specification.

Functional operation should be restricted to the conditions described under normal operating conditions.

(2)Ta=25±2°C

(3)Test condition: LED Current 420mA



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4.0 INTERFACE CONNECTION

4.1 Electrical Interface Connection

The electronics interface connector is W05025-40P-H OR equal

The connector interface pin assignments are listed in Table 5

<Table 5. Pin Assignments for the Interface Connector>

Pin No	Symbol	Description	Remark
1	RXE3P	Positive Transmission data of Pixel 3 (EVEN)	LVDS 接口 EVEN 差分对
2	RXE3N	Negative Transmission data of Pixel 3 (EVEN)	
3	RXECP	Positive Transmission Clock (EVEN)	
4	RXECN	Negative Transmission Clock (EVEN)	
5	RXE2P	Positive Transmission data of Pixel 2 (EVEN)	
6	RXE2N	Negative Transmission data of Pixel 2 (EVEN)	
7	GND	Power Ground	共地
8	RXE1P	Positive Transmission data of Pixel 1 (EVEN)	LVDS 接口 EVEN 差分对
9	RXE1N	Negative Transmission data of Pixel 1 (EVEN)	
10	RXE0P	Positive Transmission data of Pixel 0 (EVEN)	
11	RXE0N	Negative Transmission data of Pixel 0 (EVEN)	
12	RXO3P	Positive Transmission data of Pixel 3 (ODD)	LVDS 接口 ODD 差分对
13	RXO3N	Negative Transmission data of Pixel 3 (ODD)	
14	GND	Power Ground	共地
15	RXOCP	Positive Transmission Clock (ODD)	LVDS 接口 ODD 差分对
16	RXOCN	Negative Transmission Clock (ODD)	
17	GND	Power Ground	共地
18	RXO2P	Positive Transmission data of Pixel 2 (ODD)	LVDS 接口 ODD 差分对
19	RXO2N	Negative Transmission data of Pixel 2 (ODD)	
20	RXO1P	Positive Transmission data of Pixel 1 (ODD)	
21	RXO1N	Negative Transmission data of Pixel 1 (ODD)	
22	RXO0P	Positive Transmission data of Pixel 0 (ODD)	
23	RXO0N	Negative Transmission data of Pixel 0 (ODD)	
24	GND	Power Ground	共地
25	NC	SDA for BOE use, this pin should be open	BOE 生产使用，客户端不连接
26	NC	SCL for BOE use, this pin should be open	
27	NC	MTP for BOE use, this pin should be open	
28	LCD_VCC	LCD Power Supply, 3.3V	LCD 供电电压 3.3±0.3V
29	LCD_VCC	LCD Power Supply, 3.3V	
30	LCD_VCC	LCD Power Supply, 3.3V	

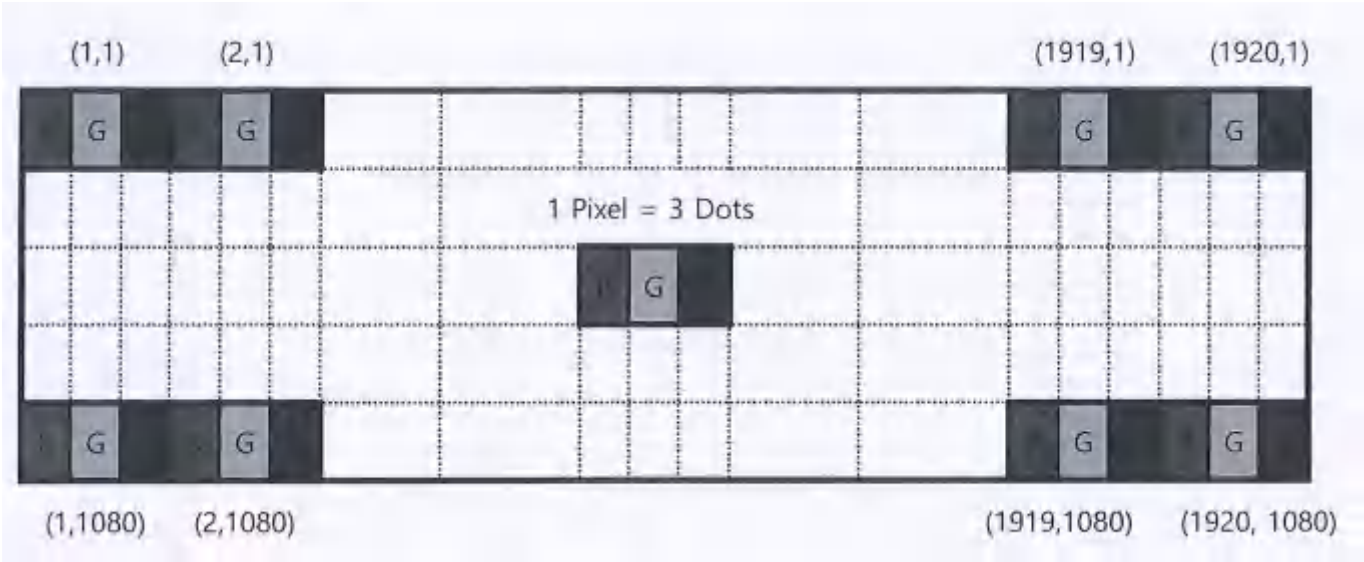
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31	LED PWM (NC)	BacklightAdjust,3.3v(3v~3.6V) 液晶背光外拉时此 PIN 脚客户端不连接	高电平 3.3V，低电平:0V
32	LED EN (NC)	Enable Pin, 3.3V (3V~3.6V) 液晶背光外拉时此 PIN 脚客户端不连接	高电平 3.3V:on, 低电平 0V:off
33	GND	Power Ground	共地
34	NC	No connect	
35	NC	No connect	
36	GND	Power Ground	共地
37	BL POWER (NC)	LED Power Supply,12V(typical) 液晶背光外拉时此 PIN 脚客户端不连接	背光供电电压 12±1.2V
38	BL POWER (NC)	LED Power Supply,12V(typical) 液晶背光外拉时此 PIN 脚客户端不连接	
39	BL POWER (NC)	LED Power Supply,12V(typical) 液晶背光外拉时此 PIN 脚客户端不连接	
40	BL POWER (NC)	LED Power Supply,12V(typical) 液晶背光外拉时此 PIN 脚客户端不连接	

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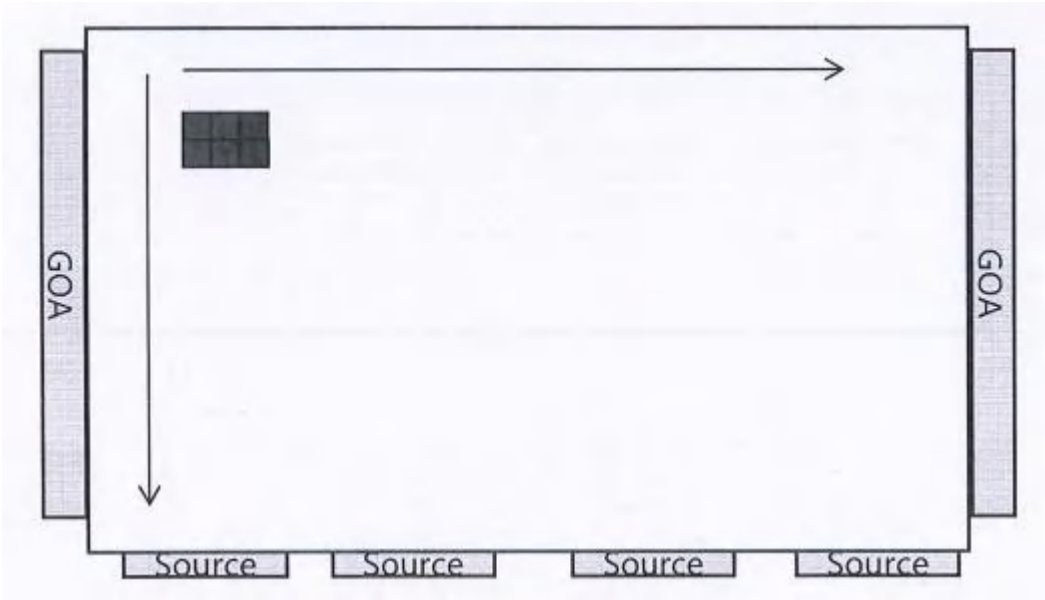
4.2 Data Input Format

Figure 3. Pixel Format



Display Position of Input Data(V-H)

Figure 4. Scan direction





5.0 SIGNAL TIMING SPECIFICATIONS

5.1.1The DCE156FH0-B01-1000 code is operated by the DE only

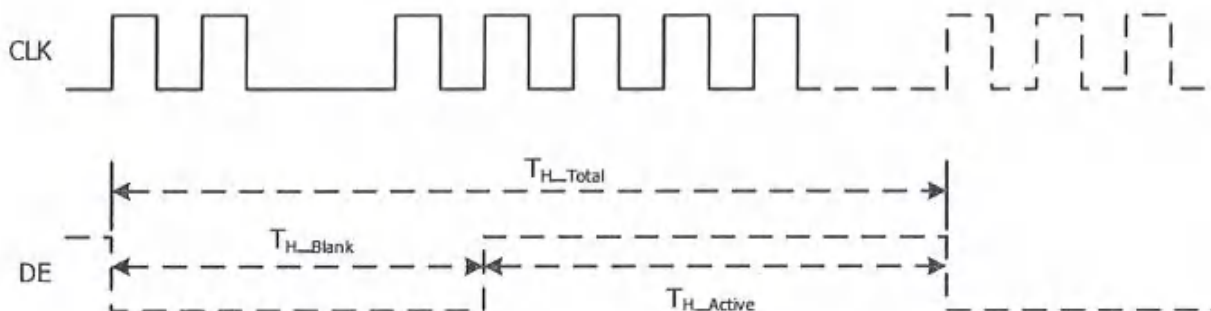
< Table 8.Timing Parameter >

Item	Symbols		Min	Typ	Max	Unit	Note
DCLK	Frequency	-	72.7	75.3	77.8	MHz	
Hsync	Period	T_{V_Total}	1001	1112	1140	tCLK	
	Horizontal Valid	t_{H_Active}	-	960	-	tCLK	
	Horizontal Blank	t_{HBlank}	44	152	180	tCLK	
Vsync	Period	T_{V_Total}	1110	1128	1170	tHP	
	Vertical Valid	T_{v_Active}	-	1080	-	tHP	
	Vertical Blank	T_{V_Blank}	30	48	90	tHP	
	Frequency	fV	58	60	62	Hz	
LVDS Receiver clock	Input spread spectrum ratio	SSr	-3	-	+3	%	

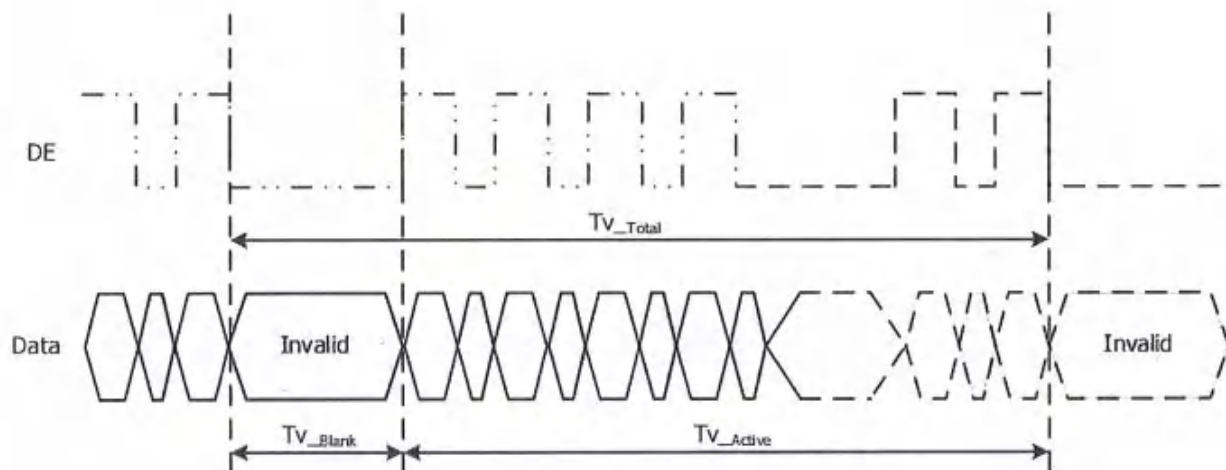
*2 Port LVDS

5.0 SIGNAL TIMING SPECIFICATIONS

5.1.1 Horizontal Input Timing



5.1.2 Vertical Input Timing





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5.2 LVDS Rx Interface Timing Parameter

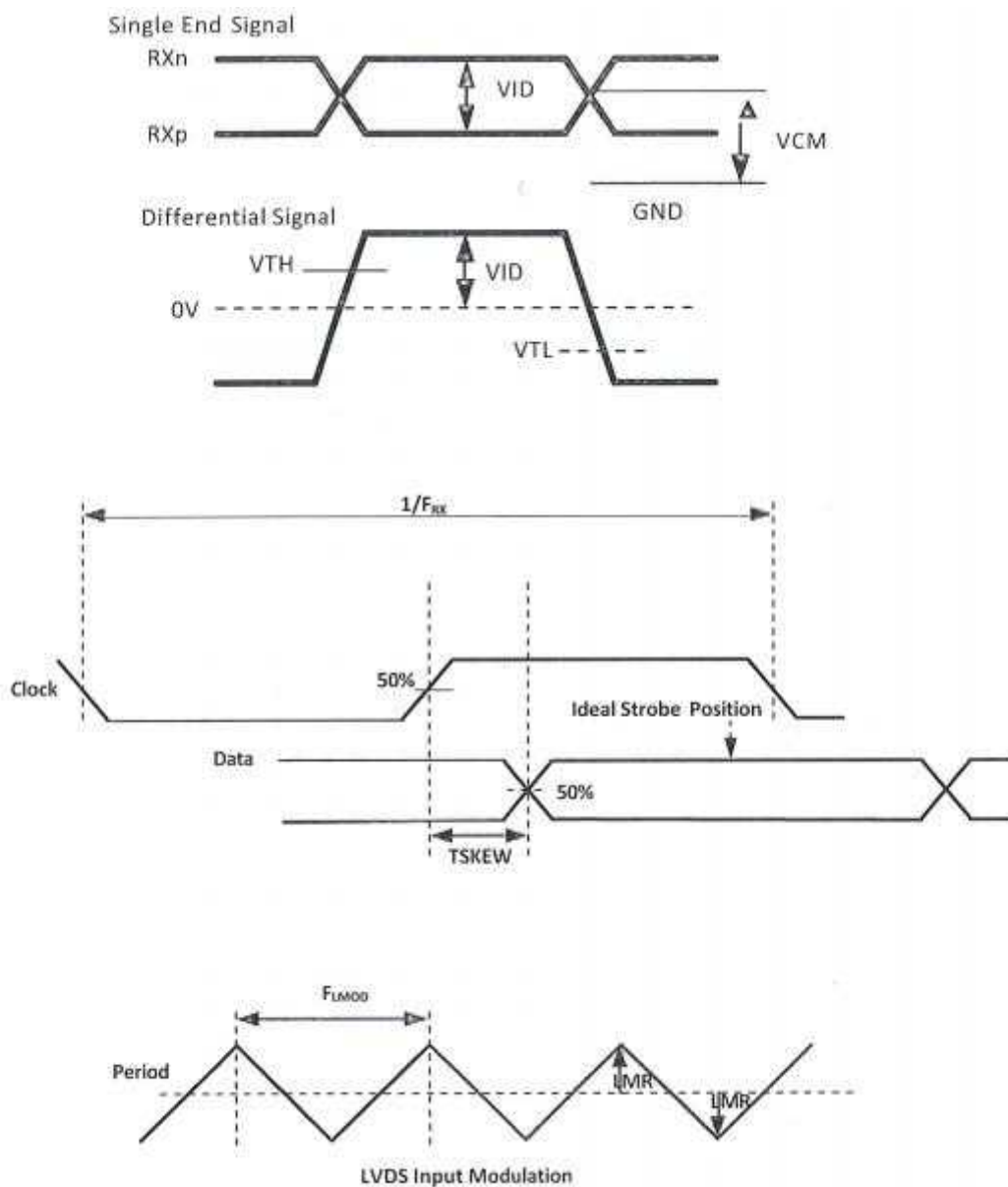
LVDSRX Specifications

<Table 9-1. LVDS RxInterface Timing Specification>

Parameter	Symbol	Condition	Rating			Unit
			Min.	Typ.	Max.	
Operation Voltage	VDD		3.3 ±5% 2.5 ±5%			V
Operation Frequency	F _{RX}		25	—	130	MHz
Differential Input Voltage	VID		100	400	600	mV
Common Mode Voltage	VCM	VDD=2.5V	0.6	1.2	1.6 - VID /2	mV
Differential Input High Threshold Voltage	VTH		—	—	100	mV
Differential Input Low Threshold Voltage	VTL		-100	—	—	mV
LVDS Channel-to-Channel Skew	TSKEW	40MHz~65MHz	-600	—	600	pS
LVDS Channel-to-Channel Skew	TSKEW	65MHz~90MHz	-400	—	400	pS
LVDS Channel-to-Channel Skew	TSKEW	90MHz~100MHz	-380	—	380	pS
LVDS Channel-to-Channel Skew	TSKEW	100MHz~110MHz	-250	—	250	pS
LVDS Channel-to-Channel Skew	TSKEW	110MHz~130MHz	-150	—	150	pS
Input LVDS Signal Modulation Frequency	F _{LMOD}	LMR=1%	—	—	300	kHz
Input LVDS Signal Modulation Range	LMR	F _{LMOD} ≥300kHz	-1	—	1	%
		F _{LMOD} ≥100kHz	-3	—	3	%

5.2 LVDS Rx Interface Timing Parameter

LVDSRX Specifications

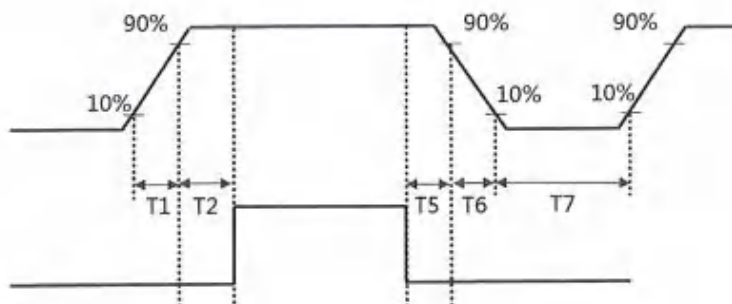


6.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below

Power Supply VDD

Interface Signal(LVDS)



Parameter	Value			Units
	Min.	Typ.	Max.	
T1	0.1	-	8	(ms)
T2	120	-	-	(ms)
T3	300	-	-	(ms)
T4	300	-	-	(ms)
T5	0	-	50	(ms)
T6	0	-	10	(ms)
T7	500	-	-	(ms)

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7.0 OPTICAL SPECIFICATIONS

7.1 Overview

The test of optical specifications shall be measured in a dark room (ambient luminance <1lux and temperature=25±2C) with the equipment of luminance meter system (Goniometer system and CA310) and test unit shall be located at an approximate distance 50cm from the polarizer at a viewing angle of 0 and 中 equal to 0°. We refer to 0=0(=03) as the 3 o'clock direction(the "right"), 0=90(=012) as the 12 o'clock direction("upward"), 0=180(=09) as the 9 o'clock direction ("left") and 0=270(=06) as the 6 o'clock direction ("bottom"). While scanning 0 and/or 0, the center of the measuring spot on the Display surface shall stay fixed.

The backlight should be operating for 30 minutes prior to measurement.

7.2 Optical Specifications

Parameter		Symbol	Condition	Min	Typ	Max	Unit	Remark
Response Time		Tr	Ta= 25°C θ= 0°	-	-	18	ms	Note 4
		Tf		-	-	17	ms	
Viewing Angle Range	Horizontal	θ3	CR>10	80	85	-	Deg	Note 1
		θ9		80	85	-		
	Viewing	θ12		80	85	-		
		θ6		80	85	-		
Color Chromaticity		Wx	θ= 0°	0.290	-	0.390		Note 3
		Wy		0.290	-	0.390		
		Rx			TBD			
		Ry			TBD			
		Gx			TBD			
		Gy			TBD			
		Bx			TBD			
		By			TBD			
Contrast Ratio		CR	θ= 0°	1000	1200	-		Note 2
Luminance uniformity		YU		75	-	-	%	

Notes : 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 5).

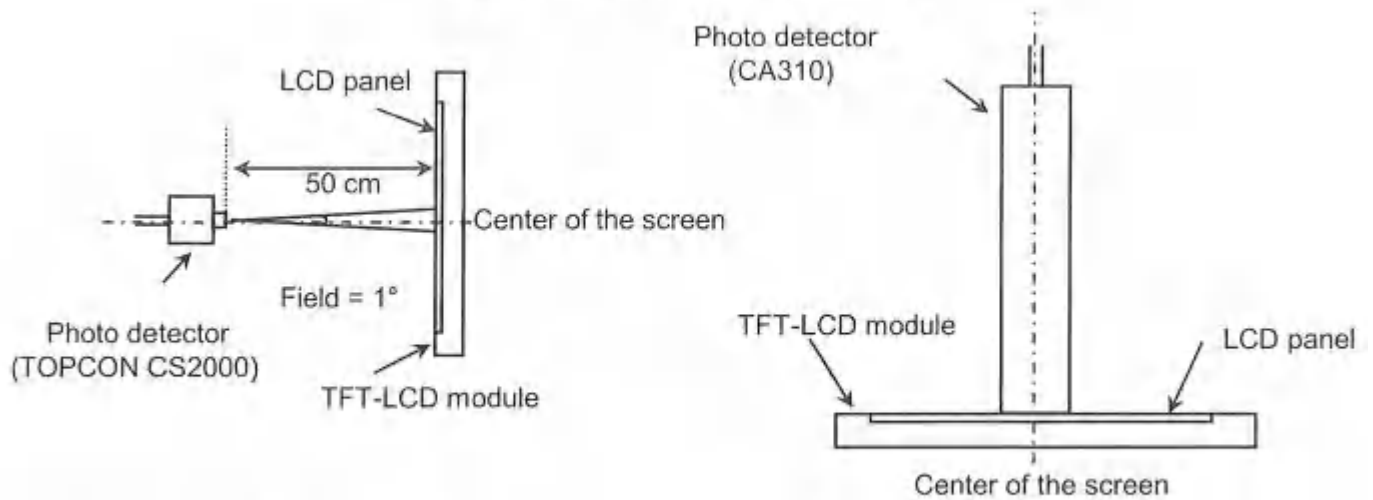
2. Contrast measurements shall be made at viewing angle of $\theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see FIGURE 5) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. The color chromaticity coordinates specified in Table 10. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

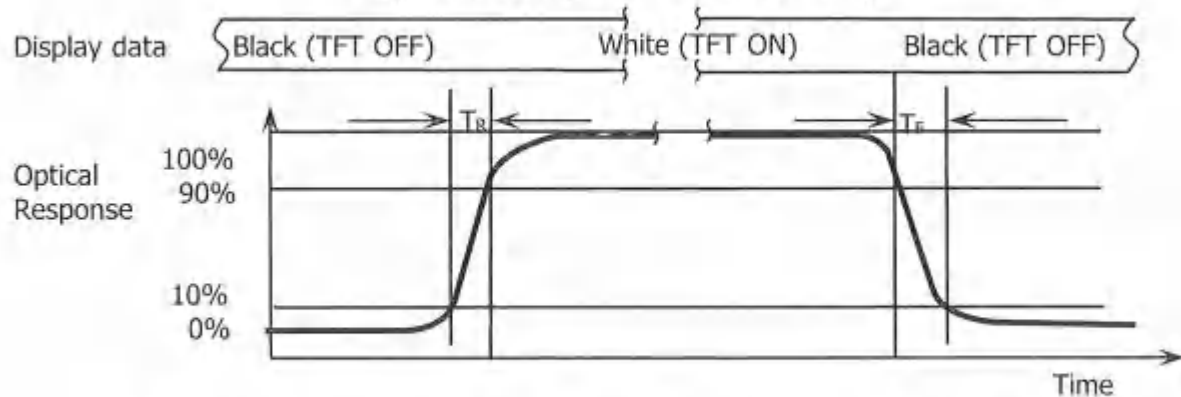
4. The electro-optical response time measurements shall be made as FIGURE 6 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_d .

Figure 5. Measurement Set Up

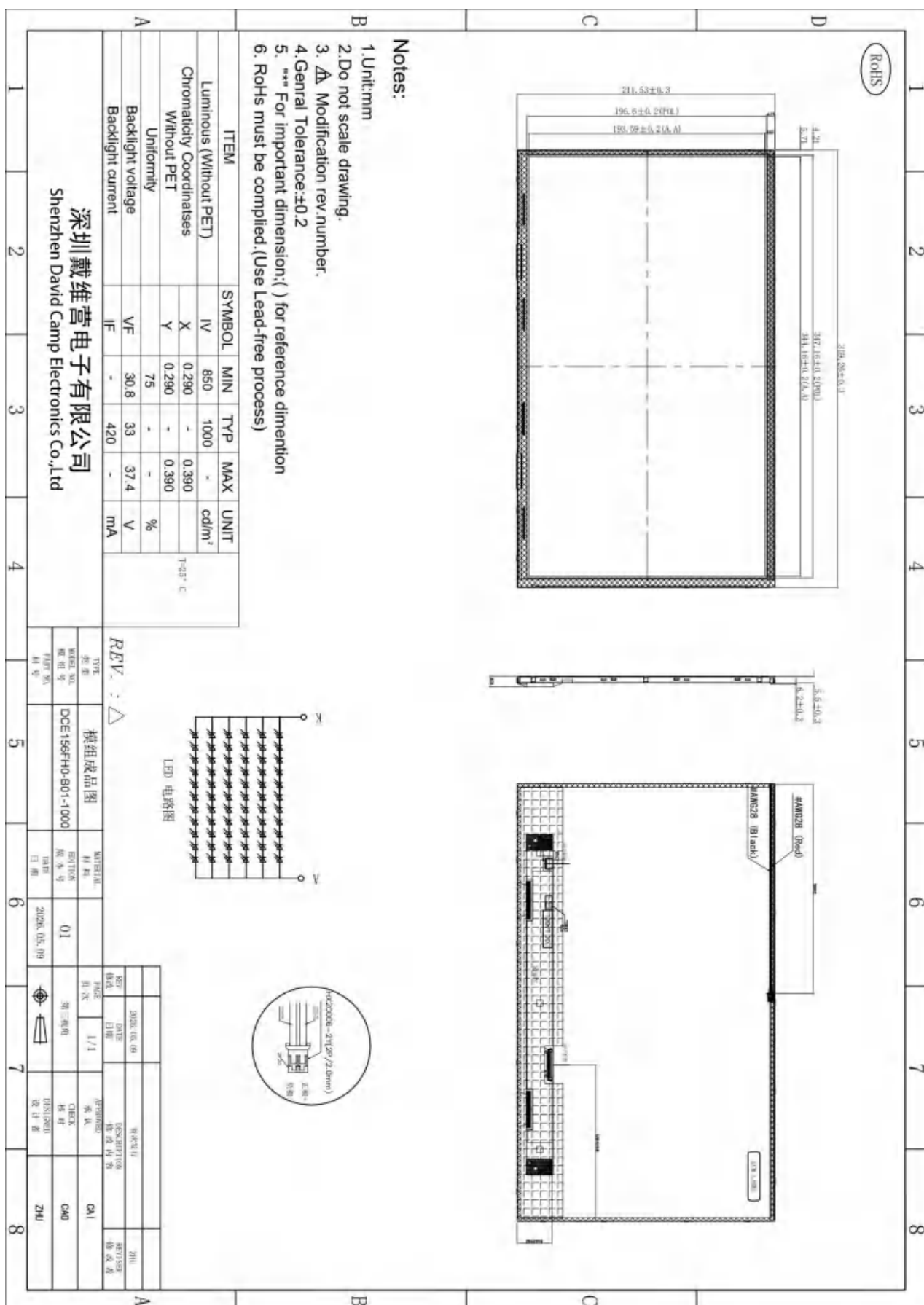


View angel range, uniformity, etc. measurement setup Flicker, measurement setup

Figure 6. Response Time Testing



The electro-optical response time measurements shall be made as shown in FIGURE 6 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr and 90% to 10% is Td.





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9.0 Reliability Test

NO	Item	Conditions	Remark
1	High Temperature Storage test(HTS)	80°C, 48hrs, Non-operationa	(1) (2)
2	Low Temperature Storage test(LTS)	-30°C,48hrs,Non-operational	
3	High Temperature Operating test(HTO)	70C, 48hrs, operational	
4	Low Temperature Operating test(LTO)	-20°C,48hrs, operational	
5	Thermal Humidity Storage test(THS)	60°C/90%RH,48hrs, Non-operational	
6	Thermal Cycle Storage test(TST)	-30~80°C,250cycles,1hr/cycle, Non-operational	
7	ESD	Air Voltage:±8Kv ClassB Contact Voltage:±6KV ClassB R:330ΩC:150pF	

Note 1: The test result shall be evaluated after the sample has been left at room temperature and humidity for 1 hours. These defects can't be accepted:1.Air bubble 2.Seal leak 3.Non-display 4.Missing segments 5.Glass crack

Note 2: In the ESD test process, LCD may appear abnormal phenomenon, can be restored after restart.

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10.0 Packing Form

TBD



11.0 General Precaution

11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

11.2 Assembly Precaytton

11.2.1 Please use the mounting hole on the module side in installing and do not bending or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.

11.2.2 Please design display housing in accordance with the following guide lines.

11.2.2.1 Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.

11.2.2.2 Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. The clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.

11.2.3 Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)

11.2.4 Please do not press any parts on the rear side such as source IC, gate IC, and FPC during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.

11.2.5 Please wipe out LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.

11.2.6 Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.

11.2.7 Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

11.3 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. Manufacturer does not warrant the module, if customers disassemble or modify the module.

11.4 Breakage of LCD Panel

11.4.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.

11.4.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.

11.4.3 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

11.4.4 Handle carefully with chips of glass that may cause injury, when the glass is broken.



11.5 Absolute Maximum Ratings and Power Protection Circuit

11.5.1 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.

11.5.2 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.

11.5.3 It's recommended employing protection circuit for power supply.

11.6 Operation

11.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead. Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.

11.6.2 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.

11.6.3 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may cause deformation or color fading.

11.6.4 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzene or other adequate solvent.

11.7 Static Electricity

11.7.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.

11.7.2 Because LCD module uses CMOS-IC on TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.

11.7.3 Persons who handle the module should be grounded through adequate methods.

11.8 Disposal

When disposing LCD module, obey the local environmental regulations.

11.9 OTHERS

11.9.1 A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior.

Please do not expose LCD module direct sunlight and strong UV rays.

11.9.2 Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.

11.9.3 For the packaging box, please pay attention to the followings:

11.9.3.1 Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.

11.9.3.2 Please do not pile them up more than 6 boxes. (They are not designed so.) And please do not turn over.

11.9.3.3 Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.

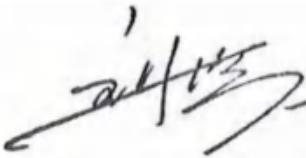
11.9.3.4 Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)

12.0 检测标准（IIS）

SUBJECT 主题：DCE156FH0-B01-1000 总成显示屏检验标准	EFFECTIVE DATE 生效日期：2026.05.09	NO 编码：WIA-PZ-009
	PAGE OF 页次：第1页 共 5 页	REV 版本：V1.0

修改登记表						
页码	1	2	3	4	5	
修订状态	V1.0	V1.0	V1.0	V1.0	V1.0	

日期	修订页	修订状态	修订内容	修订人	审核	批准
2026.05.09		V1.0	新制定	韦丹丹	刘辉	罗潮枢

会签记录		
批准	审核	制定
		

SUBJECT 主题：DCE156FH0-B01-1000 总成显示屏检验标准	EFFECTIVE DATE 生效日期：2026. 05. 09	NO 编码：WIA-PZ-009
	PAGE OF 页次：第2页 共 5 页	REV 版本：V1.0

检验环境条件	A. 温度：23±2 ℃； B. 湿度 60±10%RH.	
	C. 外观检查光照：300~700Lux. D. 点亮检查光照：150~250Lux.	
检测条件：	A. 目视距离：35~50 cm。	
	B. ADS 产品：面向 Panel 与垂直线成 45° 内°。	
	C. TN 产品：面向 Panel 与垂直线 10° 内	
检查时注意事项：	A.检查时必须配戴好防静电手环。	
	B.检查时必须配戴好防静电手套。	
	C.LCD 屏幕检查时，必需关掉电源后 FPC 进行插拔。	

抽样计划:MIL—STD—105E 的一般检验水准 II 级，正常检验、随机抽样。

接收质量限（AQL）为：CR（致命缺陷）= 0；MA(重缺陷)=0.65；MI(轻缺陷)= 1.5

检验人员视力推荐≥5.0（小数记录法≥1.0）

代码	代码含义	代码	代码含义
L	长度（mm）	N	数目（PCS）
W	宽度（mm）	S	面积（mm ² ）
D	直径（mm）	DS	距离（mm）

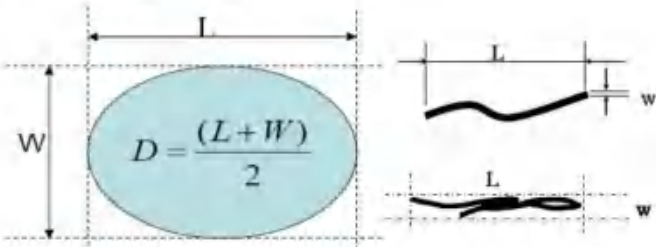


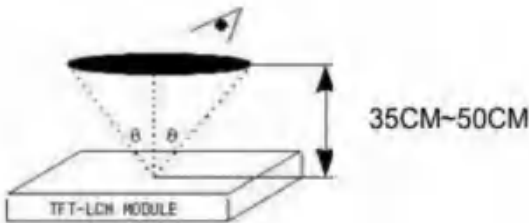
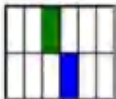
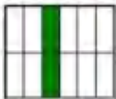
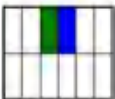
图 1. 点状异物

图 2. 线状异物

※Note 3) For pixel defect, dot means a sub-pixel. Dot defects should be larger than half size of a sub-pixel. Dot which is invisible through 5% ND filter or smaller than 1/2 of sub-pixel size will not counted as "1 dot" defect.



[2 adjacent dots defect]



检验项目	不良内容描述	判定标准	缺陷判定			测试仪 器/治具	适用 范围
			CR	MA	MI		

SUBJECT

主题：DCE156FH0-B01-1000
总成显示屏检验标准

EFFECTIVE DATE

生效日期：2026.05.09

NO

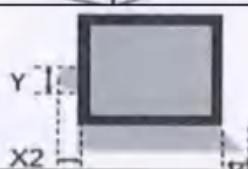
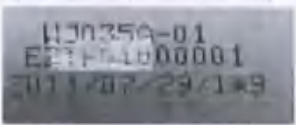
编码：W1A-PZ-009

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REV

版本：V1.0

ALL	Stain		可擦拭 OK					ALL	
PNL 相关	Crack		不允许			✓		出货状态为 Single Cell / FOG / MDL / TPM / 产品适用	
	Side Chipping		不影响功能和组装 OK				✓		
	Corner Chipping		不影响功能和组装 OK				✓		
	Burr		不影响功能和组装 OK				✓		
	凹点/异物/气泡/鱼眼/油墨点	D≤0.3mm 忽略不计 0.3mm<D≤0.5mm		N≤4			✓		
	线性/划伤/毛丝	W≤0.1mm L≤4mm 忽略不计 0.1mm<W≤0.2mm L≤4mm		N≤3			✓		
FPC/PCB 相关	短路/断路		不允许			✓		出货状态为 FOG / MDL / TPM / 产品适用	
	元器件		元器件缺失不允许				✓		
Backlight 相关	条码/喷码/标识		关键信息可适别 OK				✓	出货状态为 MDL / TPM / 产品适用	
	划伤		limit Sample/无感划伤 OK				✓		
	污渍		可擦拭 OK				✓		

目视/
菲林卡



PRODUCT GROUP	REV	Rev.01
	Ver.P0	2026/05/09

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电性 检查	像素缺陷 Pixel Defects	像素亮点 Bright Dot	$N \leq 1$			√	目视/ 菲林卡
		像素暗点 Dark Dot	$N \leq 4$				
		像素亮暗点 Bright + Dark Dot	$N \leq 5$				
		2 Adjacent Sub Pixel Defect	$N \leq 2$				
	异物/凹痕/气泡 /斑点//外来物 质/异物亮点 ForeignMaterial /Dent/ Bubble/ Spots//Extraneous Substances/Dot	$D \leq 0.3mm$	忽略不计			√	目视/ 滤光片 ND5%
		$0.3mm < D \leq 0.5mm$	$N \leq 4$				
		$W \leq 0.1mm \quad L \leq 4mm$	忽略不计			√	
		$0.1mm < W \leq 0.2 \text{ mm}$ $L \leq 4mm$	$N \leq 3$				
	Mura (亮斑、白斑、蓝斑)	按ND5%过滤，距离30CM，停留3秒，不可见OK， 可见NG				√	目视/ 滤光片 ND5%
	密集点	密集小碎点按5% NDFilter(距表面 $2 \pm 0.5cm$)) (遮盖可见NG; 遮盖不可见 判定OK)				√	
	偏光片气泡	边缘气泡未超出BM区的1/2为OK, AA区气 泡按点大小判定				√	目视/ 量尺
	成品弯曲度	用0.5mm塞规判定				√	塞规片
	膜拱	可签订限度样品管控				√	限度样 样品
	残影	先播放第一张黑白棋盘图, 停留60秒, 60 秒后切换到20%全灰图片, 如不能从全灰 图片中看出残影即为合格. 当出现残影 时与客户签样样机对比, 严重程度不超 过签样样机且30秒后残影消失可为接 受。				√	目视/ 滤光片 ND5%
边缘光晕 (压黄)	在规定的测试照度条件下, 使用 5%ND Filter 去遮, 不可见为 OK测试方法:眼 睛到 ND flter 距离:40cm, ND filter 到产品的距离为3~5cm				√		
色温偏差	使用色彩分析仪对产品中心位置进行测 量, 测量出白色画面XY坐标值, 在规格书 内可接受。				√	CA310	
屏闪	屏显示忽亮忽暗或跳 动	不允许		√		目测/ 治具	

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	显示不全	在显示文件时文件内容显示不完整	不允许		√				
	花屏	屏显示出现不同颜色条纹或方格及马赛克现象	不允许		√				
	失真	显示颜色失真	不允许		√				
	白屏	屏幕整个显示区域为白色	不允许		√				
	黑屏	屏幕整个显示区域为黑色	不允许		√				
	亮线	显示屏出现横线/竖线	不允许		√				
	漏液晶	液晶面板因密封不严或封口未封住，使液晶外漏	不允许		√				
	显示反向	测试画面与正常显示画面相反	不允许		√				