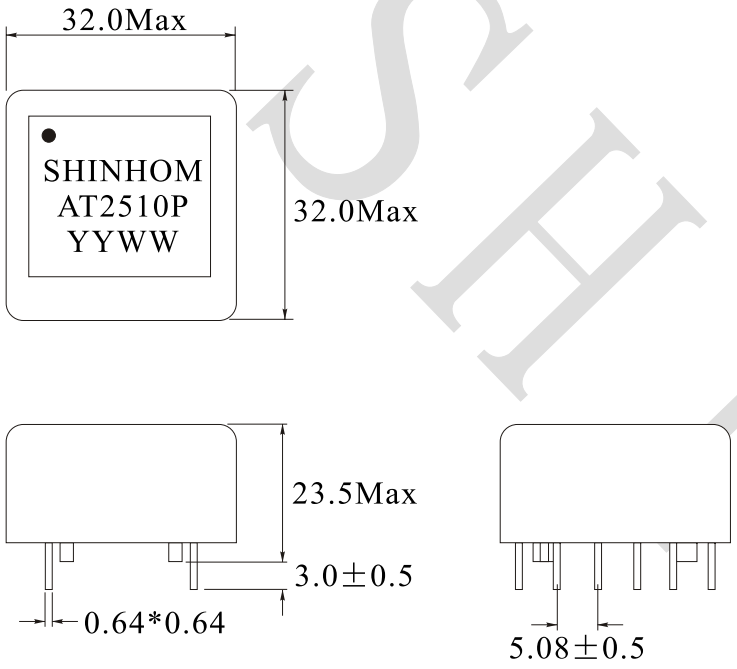
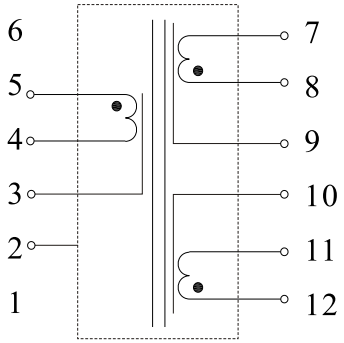
Rev.	Description	Date
A0	New release	2022.11.03

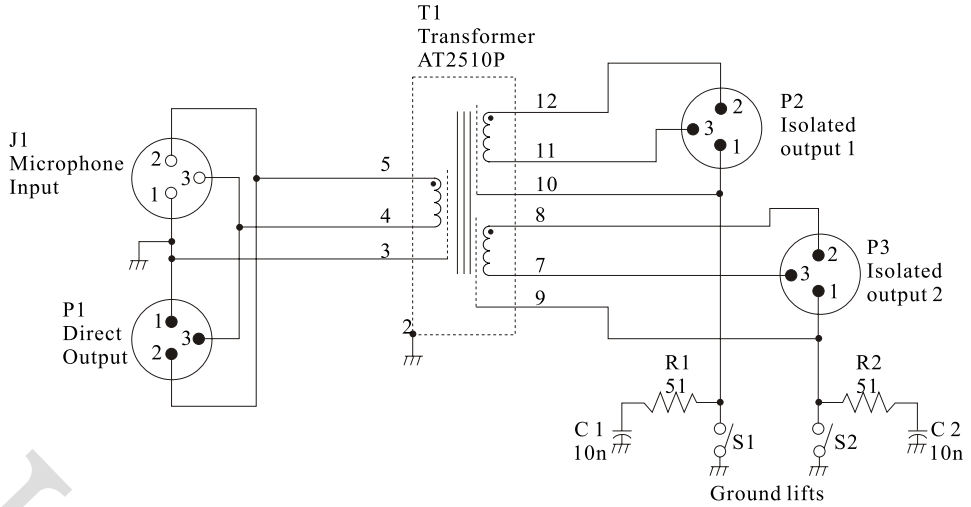
PHYSICAL CHARACTERISTICS(mm)



ELECTRONICAL SCHEMATIC



TYPICAL APPLICATION



ELECTRONICAL SPECIFICATIONS

Inductance: 12-11: 3.0H±20%@1KHz,0.3V
5-4: 3.0H±20%@1KHz,0.3V
8-7: 3.0H±20%@1KHz,0.3V

DCR: 12-11: 50Ω ±15%
5-4: 51Ω ±15%
8-7: 58Ω ±15%

Hi-Pot: 500Vac,5mA,1S Pri to Sec
500Vac,5mA,1S Coil to Core

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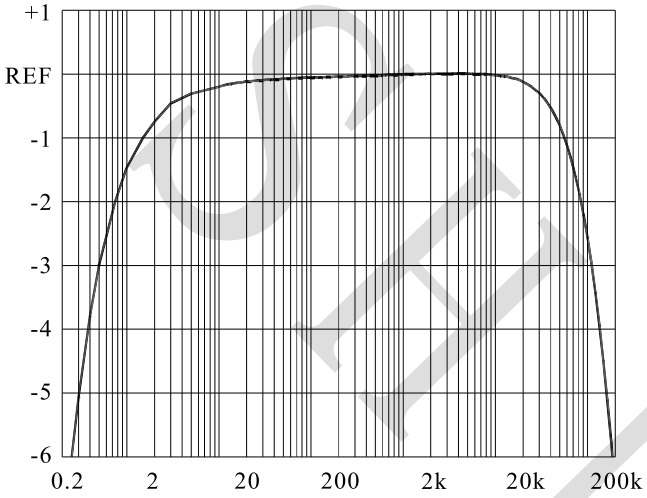
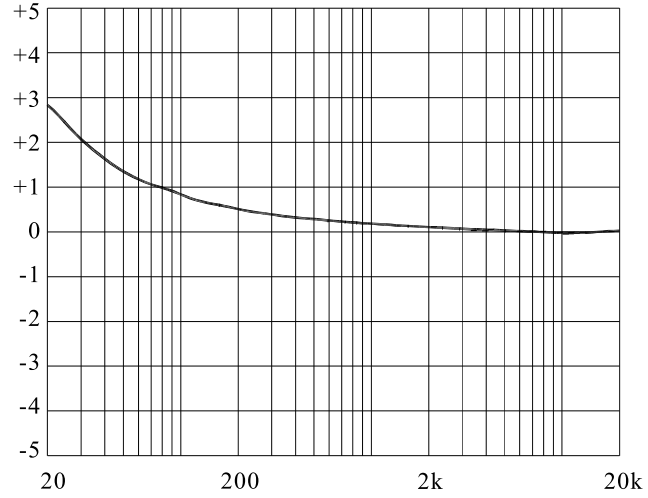
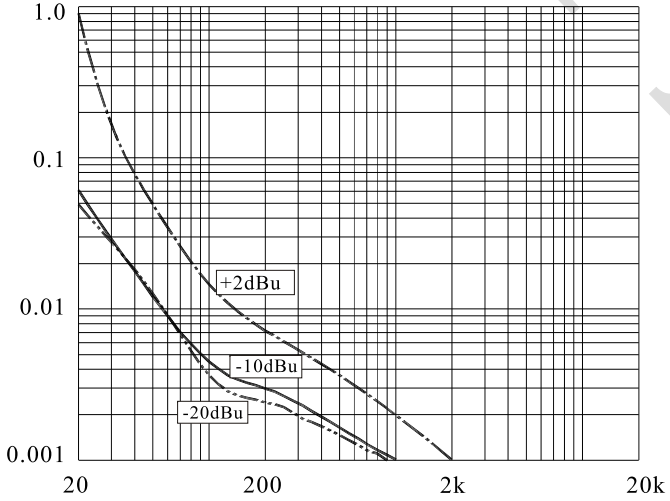
Reference level response Amplitude(dB) vs Frequency(Hz)



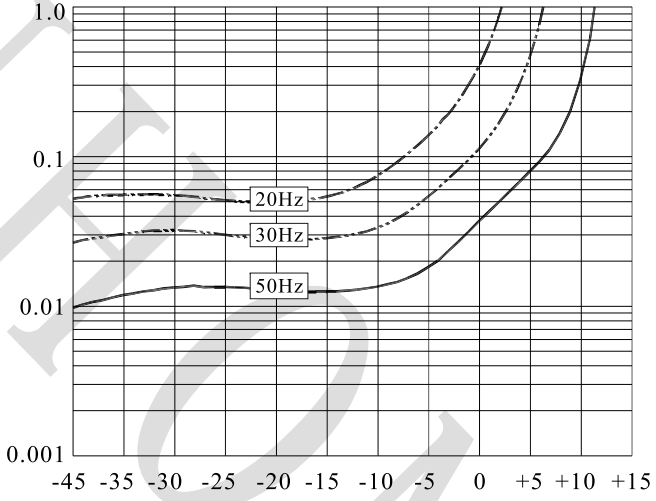
Deviation from linear phase Deviation(Deg)vs Frequency(Hz)



THD at Fixed input levels THD+N(%) vs Frequency(Hz)



THD at Fixed Frequency(Hz) THD+N(%) vs input levels(dBu)



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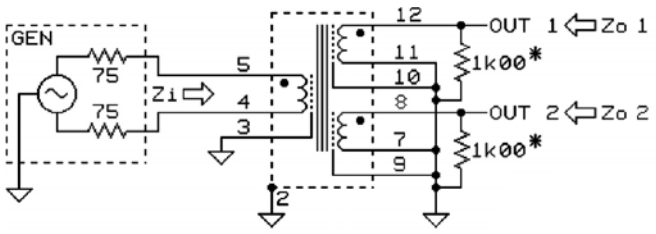
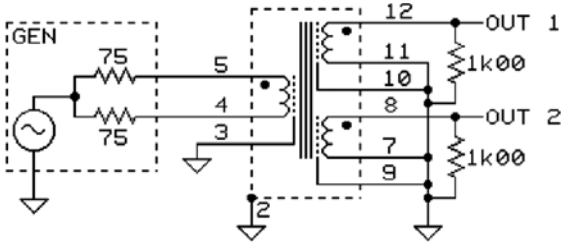
PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, Zi	1 kHz, –20 dBu, test circuit 1	570 Ω	578 Ω	590 Ω
Voltage gain	1 kHz, –20 dBu, test circuit 1	–1.4 dB	–1.2 dB	–1.0 dB
Magnitude response, ref 1 kHz	20 Hz, –20 dBu, test circuit 1	–0.50 dB	–0.12 dB	±0.0 dB
	20 kHz, –20 dBu, test circuit 1	–0.25 dB	–0.14 dB	+0.1 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, –20 dBu, test circuit 1		+2.8/–0°	±5.0°
Distortion (THD)	1 kHz, –20 dBu, test circuit 1		0.001%	
	20 Hz, –20 dBu, test circuit 1		0.05%	0.15%
Maximum 20 Hz input level	1% THD, test circuit 1	0 dBu	+2.0 dBu	
Common-mode rejection ratio (CMRR) 150 Ω balanced source	60 Hz, test circuit 2		130 dB	
	3 kHz, test circuit 2	85 dB	96 dB	
Output impedance, Zo	1 kHz, test circuit 1		256 Ω	
DC resistances	primary (5 to 4)		52 Ω	
	secondary (12 to 11)		48 Ω	
	secondary (8 to 7)		55 Ω	
Capacitances @ 1 kHz	primary (5 and 4) to 2,3,9 and 10		125 pF	
	secondary (12 and 11) to 2,3,9 and 10		60 pF	
	secondary (8 and 7) to 2,3,9 and 10		87 pF	
Turns ratio	primary to either secondary	1:0.999	1:1.000	1:1.001
Temperature range	operation or storage	0° C		70° C
Breakdown voltage (see IMPORTANT NOTE below)	primary or secondary to shield and case, 60 Hz, 1 minute test duration	250 V RMS		



TEST CIRCUIT 1

*NOT USED FOR EACH RESPECTIVE Zo TEST



TEST CIRCUIT 2

IMPORTANT NOTE:

This device is NOT intended for use in life support systems or any application where its failure could cause injury or death. The breakdown voltage specification is intended to insure integrity of internal insulation systems; continuous operation at these voltages is NOT recommended. Consult our applications engineering department if you have special requirements.

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