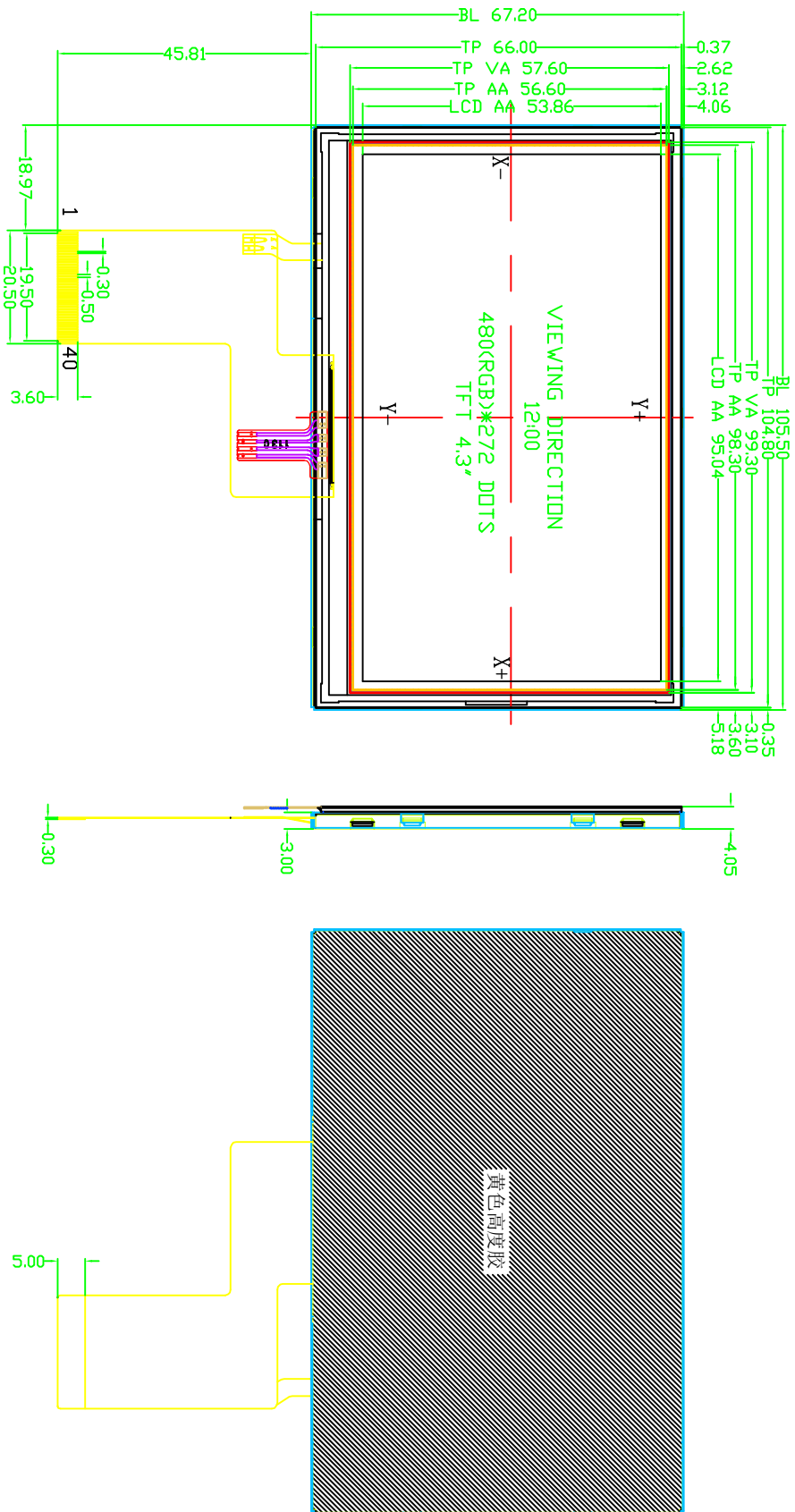


REV.	V1.0	FILE NO.	ST043G-LCM-V1.0
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Pin	Pin Name
1	LEDK
2	LEDA
3	GND
4	VCC
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	DCLK
31	DISP
32	HSYNC
33	VHsync
34	DEN
35	AVDD(INC)
36	GND
37	XRNC)
38	YDNC)
39	XL(INC)
40	Y(INC)



CIRCUIT DIAGRAM (LED $7 \times 1 = 7$ dies)

A schematic diagram of a ladder network. It consists of a vertical line with two terminals, 'A' at the top and 'K' at the bottom. Between these terminals, there are six resistors connected in series. Each resistor is represented by a rectangle with diagonal lines. The resistors are connected to the vertical line at their top and bottom terminals, and their left terminals are connected to a common horizontal line that runs across the top and bottom of the network.

Specificogion:

- 1). Viewing angle : 12:00
- 3). Display mode: TFT/TRANSMISSIVE
- 4). Operating temp.: -20°C-70°C
- 5). Storage temp.: -30°C-80°C
- 6). IC: ST7282 or compatible
- 7). Backlight: side, 7 PCS white DIES, VLED=21.0V, ILED=20mA
- 8). All the raw materials are RoHS compliant

		深圳市顺世科技有限公司 SHENZHEN SUNNY TECHNOLOGY CO., LTD	
DRAWN BY:	2018.03.12	ST043G	
CHECK BY:		SCALE:	MATERIAL:
APPROVER:		UNIT: mm	1:1
		TOLERANCE: ±0.2	PAGE: 1 of 1
			